

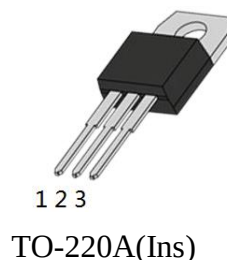


T835H-800A

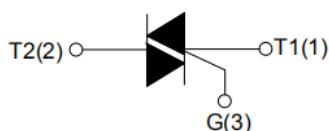
Silicon Controlled Rectifier

Features

- Blocking Voltage to 800V
- Glass Passivated Surface for Reliability and Uniformity
- RoHS Compliant & HF
- High Dv/Dt Rate
- $I_{T(RMS)}$ to 8A of Triacs
- High Junction Temperature and High Environment Temperature Condition



Pin Configuration



Absolute Maximum Ratings (Tc=25°C Unless otherwise specified)

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40~150	°C
Operating junction temperature range	T_j	-40~150	°C
Repetitive peak off-state voltage (Tj=25°C)	V_{DRM}	800	V
Repetitive peak reverse voltage (Tj=25°C)	V_{RRM}	800	V
RMS on-state current	$I_{T(RMS)}$	8	A
Non repetitive surge peak on-state current (full cycle, F=50Hz)	I_{TSM}	80	A
I^2t value for fusing (tp=10ms)	I^2t	32	A ² s
Critical rate of rise of on-state current (IG=2×IGT)	dI/dt	50	A/μs
Peak gate current	I_{GM}	4	A
Average gate power dissipation	$P_{G(AV)}$	1	W

Peak gate power	P_{GM}	5	W
Thermal Resistance(between Junction and Case)	$R_{\theta(J-C)}$	3.5 (Typ.)	°C/W

Electronics Characteristics (T_c=25°C Unless otherwise specified)

3 Quadrants:

Parameter	Symbol	Quadrant		Value	Unit
				T835	
Gate Trigger Current (Continuous dc) @VD=12V, RL=33Ω	I _{GT}	I - II - III	MAX	35	mA
Gate Trigger Voltage (Continuous dc) @VD=12V, RL=33Ω	V _{GT}			1.5	V
Gate non-trigger voltage@VD=V _{DRM}	V _{GD}	I - II - III	MIN	0.2	V
Holding Current@IT=500mA	I _H	-	MAX	50	mA
Latching Current@IG=1.2IGT	I _L	I - III	MAX	80	mA
		II		100	
Critical Rate-of-Rise of Off State Voltage @VD=0.66×V _{DRM} , T _j =150°C, Gate Open	dV/dt	-	MIN	1000	V/μs
Peak Forward On-State Voltage @ITM=11A, tp=380μs, T _j =25°C	V _{TM}	-	MAX	1.55	V
Peak Repetitive Forward @V _{DRM} =V _{VRRM} , T _j =25°C	I _{DRM}	-	MAX	5	μA
Reverse Blocking Current @V _{DRM} =V _{VRRM} , T _j =150°C	I _{RRM}	-	MAX	2	mA

Note: The above typical parameters or typical characteristics are only indicative and do not make specific guarantees. If detailed values are required, additional communication and provision are required.

FIG.1: Maximum power dissipation versus RMS on-state current

P(W)

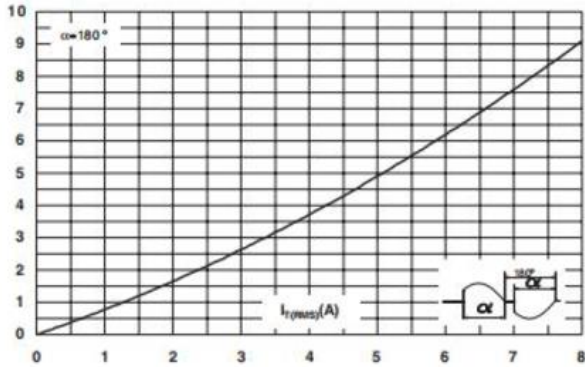


FIG.3: Surge peak on-state current versus number of cycles

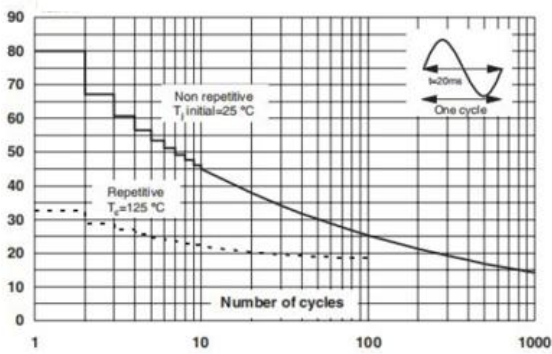
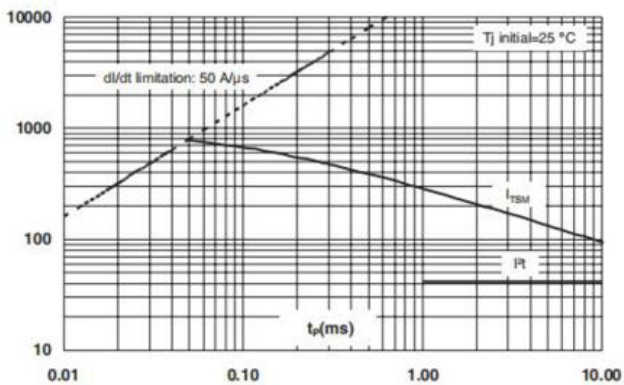
 $I_{TSM}(A)$ FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10ms$, and corresponding value of $I^2 t$ $I_{TSM}(A), I^2 t (A^2s)$ 

FIG.2: RMS on-state current versus case temperature in different packaging

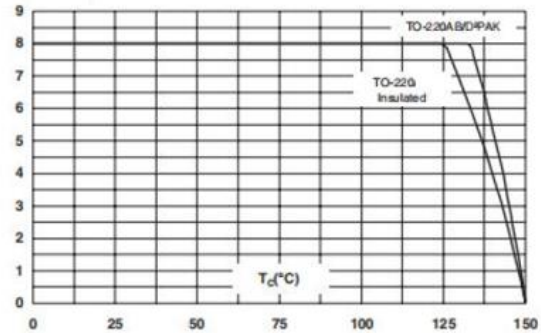
 $I_{T(RMS)}(A)$ 

FIG.4: On-state characteristics (maximum values)

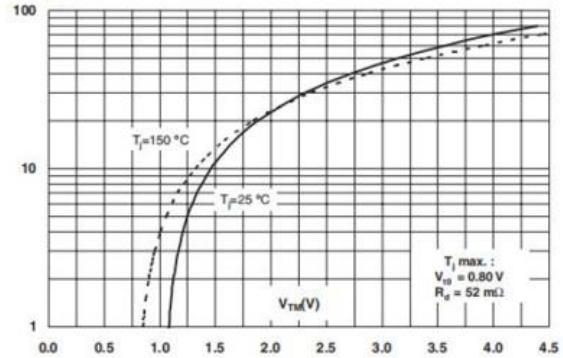
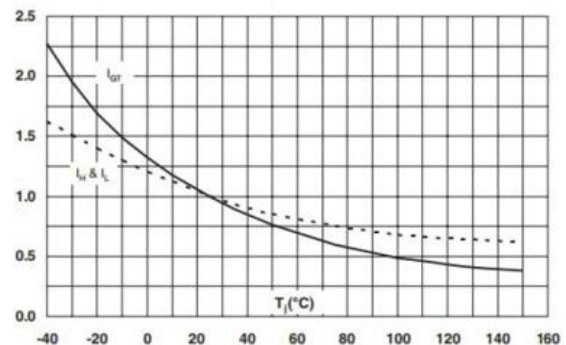
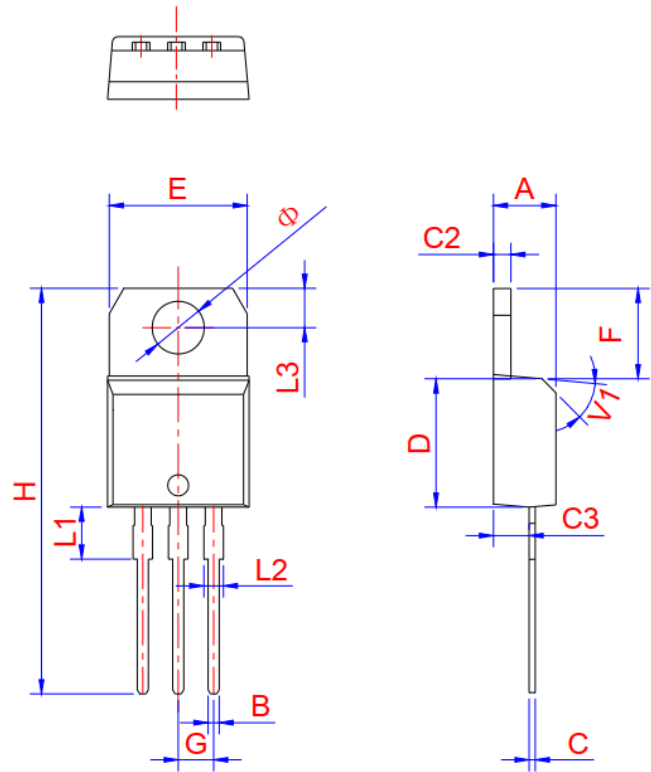
 $I_{TM}(A)$ 

FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

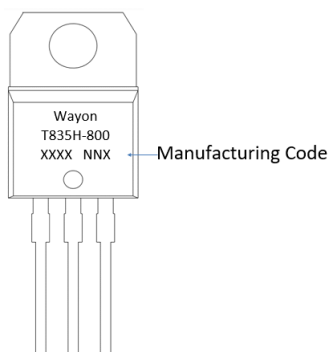
 $I_{GT}, I_H, I_L(T_J) / I_{GT}, I_H, I_L(T_J=25^\circ C)$ 

Outline Drawing

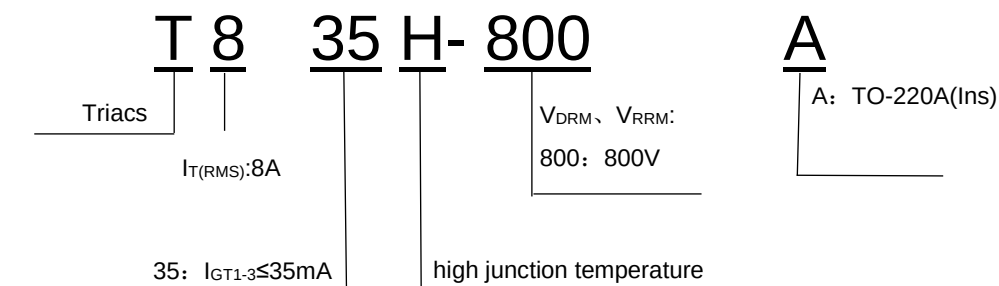
SYMBOL	MM		
	MIN	NOM	MAX
A	4.20	4.47	4.60
B	0.61	-	0.93
C	0.40	0.50	0.70
C2	1.20	1.27	1.48
C3	2.20	-	2.75
D	8.60	-	9.90
E	9.70	-	10.60
F	6.00	-	7.15
G	-	2.54	-
H	28	-	29.8
L1	-	3.50	-
L2	1.10	-	1.70
L3	2.55	-	2.95
V1	-	45°	-
Φ	3.65	3.75	3.85



Marking Code:



Part Number System



Package Information

Package	Base qty.	Delivery mode
TO-220A(Ins)	50	Tube

Contact Information

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