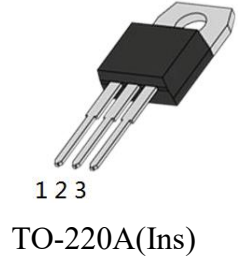
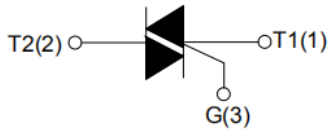


Features

- Blocking Voltage to 800V
- Glass Passivated Surface for Reliability and Uniformity
- RoHS Compliant & HF
- High Dv/Dt Rate
- $I_{T(RMS)}$ to 30A of Triacs
- High Junction Temperature and High Environment Temperature Condition



Pin Configuration



Absolute Maximum Ratings (Tc=25°C Unless otherwise specified)

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40~150	°C
Operating junction temperature range	T_j	-40~150	°C
Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	800	V
Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	800	V
RMS on-state current	$I_{T(RMS)}$	30	A
Non repetitive surge peak on-state current (full cycle, F=50Hz)	I_{TSM}	270	A
I^2t value for fusing ($t_p=10\text{ms}$)	I^2t	365	A ² s
Critical rate of rise of on-state current ($I_G=2\times I_{GT}$)	dI/dt	50	A/ μs
Peak gate current	I_{GM}	4	A
Average gate power dissipation	$P_{G(AV)}$	1	W

Peak gate power	P_{GM}	10	W
Thermal Resistance(between Junction and Case) @TO-220A(Ins)	$R_{\theta(J-C)}$	1.7 (Typ.)	°C/W

Electronics Characteristics (Tc=25°C Unless otherwise specified)

3 Quadrants:

Parameter	Symbol	Quadrant		Value	Unit
				T3035	
Gate Trigger Current (Continuous dc) @VD=12V, RL=33Ω	I_{GT}	I - II - III	MAX	35	mA
Gate Trigger Voltage (Continuous dc) @VD=12V, RL=33Ω	V_{GT}			1.5	V
Gate non-trigger voltage@VD=VDRM	V_{GD}	I - II - III	MIN	0.2	V
Holding Current@IT=500mA	I_H	-	MAX	60	mA
Latching Current@IG=1.2IGT	I_L	I - III	MAX	80	mA
		II		120	
Critical Rate-of-Rise of Off State Voltage @VD=0.66×VDRM, Tj=150°C, Gate Open	dV/dt	-	MIN	500	V/μs
Peak Forward On-State Voltage @ITM=35A, tp=380μs, Tj=25°C	V_{TM}	-	MAX	1.6	V
Peak Repetitive Forward @VDRM=VRRM, Tj=25°C	I_{DRM}	-	MAX	10	μA
Reverse Blocking Current @VDRM=VRRM, Tj=150°C	I_{RRM}	-	MAX	8.5	mA

Note: The above typical parameters or typical characteristics are only indicative and do not make specific guarantees. If detailed values are required, additional communication and provision are required.

FIG.1: Maximum power dissipation versus RMS on-state current

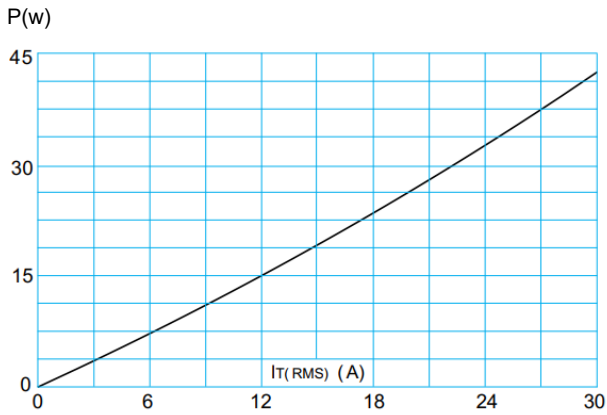


FIG.3: Surge peak on-state current versus number of cycles

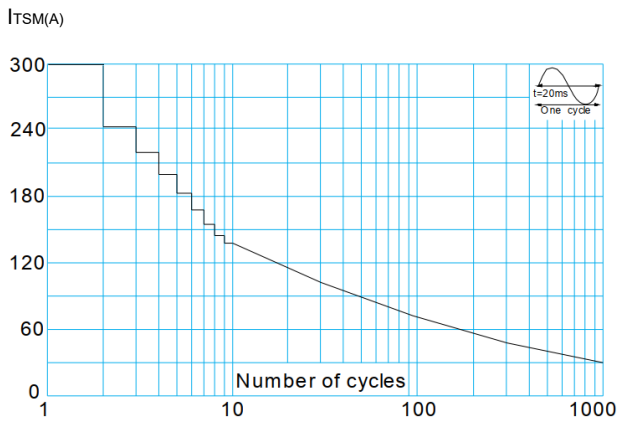


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20ms$, and corresponding value of $I^2 t$ (I - II - III: $dI/dt < 50A/\mu s$; IV: $dI/dt < 10A/\mu s$)

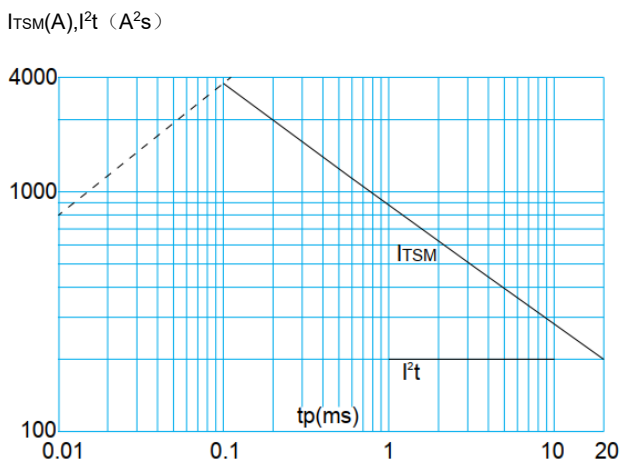


FIG.2: RMS on-state current versus case temperature in different packaging

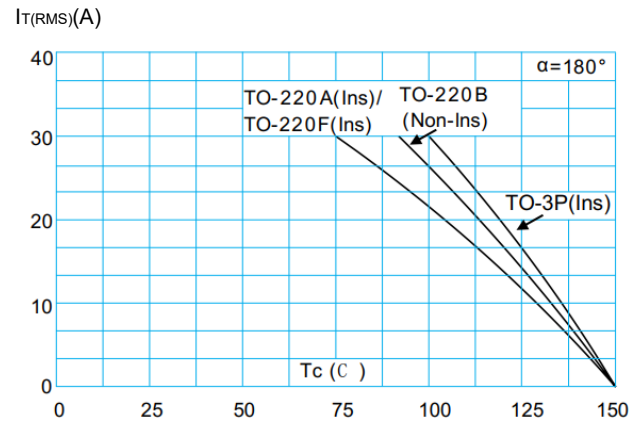


FIG.4: On-state characteristics (maximum values)

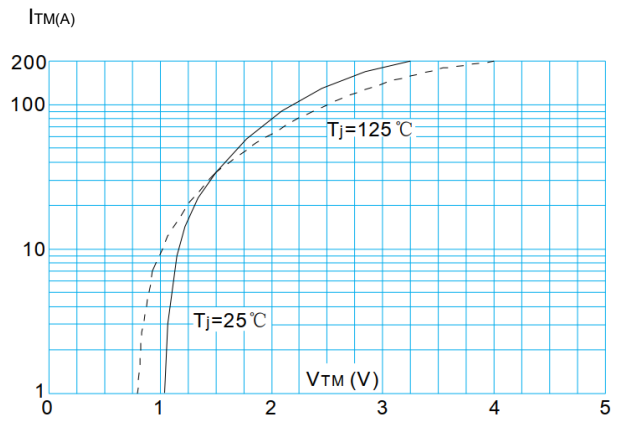
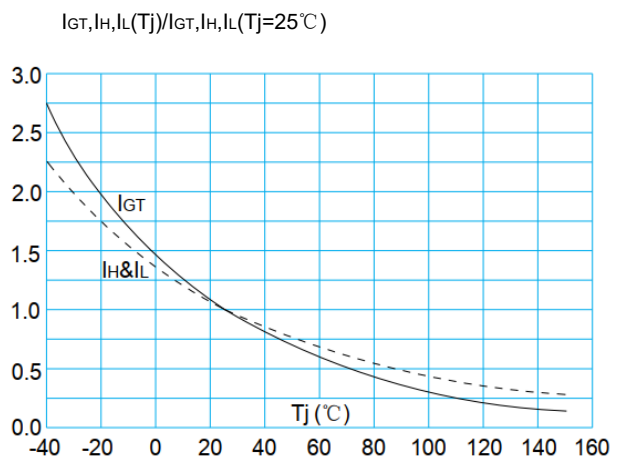
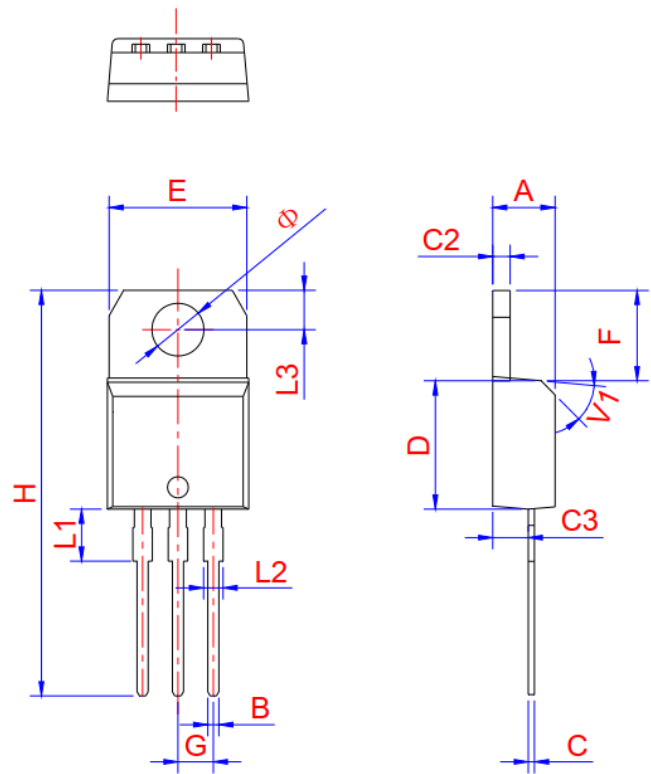


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

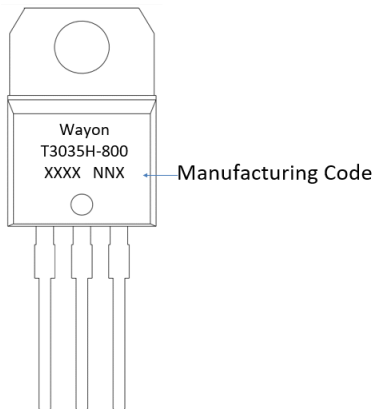


Outline Drawing- TO-220A Ins

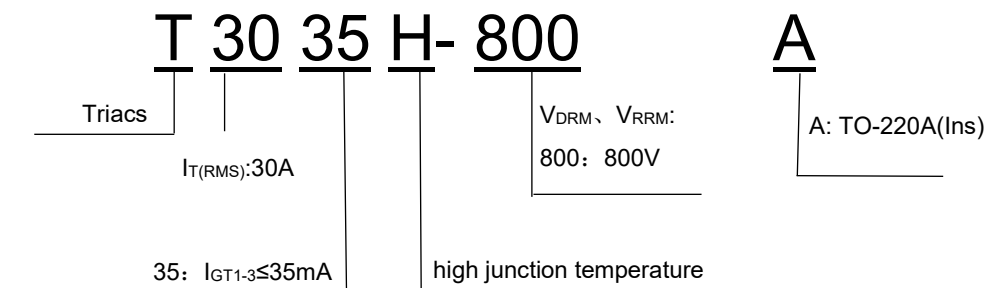
SYMBOL	MM		
	MIN	NOM	MAX
A	4.20	4.47	4.60
B	0.61	-	0.93
C	0.40	0.50	0.70
C2	1.20	1.27	1.48
C3	2.20	-	2.75
D	8.60	-	9.90
E	9.70	-	10.60
F	6.00	-	7.15
G	-	2.54	-
H	28	-	29.8
L1	-	3.50	-
L2	1.10	-	1.70
L3	2.55	-	2.95
V1	-	45°	-
Φ	3.65	3.75	3.85



Marking Code:



Part Number System



Package Information

Package	Base qty.	Delivery mode
TO-220A(Ins)	50	Tube

Contact Information

No.1001, Shiwan(7) Road, Pudong District, Shanghai, P.R.China.201207

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WAYON website: <http://www.way-on.com>

For additional information, please contact your local Sales Representative.

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