

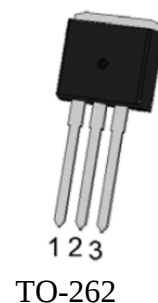


T2035H-800N

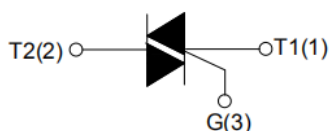
Silicon Controlled Rectifier

Features

- Blocking Voltage to 800V
- Glass Passivated Surface for Reliability and Uniformity
- RoHS Compliant & HF
- High Dv/Dt Rate
- $I_{T(RMS)}$ to 20A of Triacs
- High Junction Temperature and High Environment Temperature Condition



Pin Configuration



Absolute Maximum Ratings (Tc=25°C Unless otherwise specified)

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40~150	°C
Operating junction temperature range	T_j	-40~150	°C
Repetitive peak off-state voltage (Tj=25°C)	V_{DRM}	800	V
Repetitive peak reverse voltage (Tj=25°C)	V_{RRM}	800	V
RMS on-state current	$I_{T(RMS)}$	20	A
Non repetitive surge peak on-state current (full cycle, F=50Hz)	I_{TSM}	200	A
I^2t value for fusing (tp=10ms)	I^2t	200	A ² s
Critical rate of rise of on-state current (IG=2×IGT)	dI/dt	50	A/μs
Peak gate current	I_{GM}	4	A
Average gate power dissipation	$P_{G(AV)}$	1	W

Peak gate power	P_{GM}	5	W
Thermal Resistance(between Junction and Case)	$R_{\theta(J-C)}$	1 (Typ.)	°C/W

Electronics Characteristics (Tc=25°C Unless otherwise specified)

3 Quadrants:

Parameter	Symbol	Quadrant		Value	Unit
				T2035	
Gate Trigger Current (Continuous dc) @VD=12V, RL=33Ω	I_{GT}	I - II - III	MAX	35	mA
Gate Trigger Voltage (Continuous dc) @VD=12V, RL=33Ω	V_{GT}			1.5	V
Gate non-trigger voltage@VD=VDRM	V_{GD}	I - II - III	MIN	0.2	V
Holding Current@IT=500mA	I_H	-	MAX	40	mA
Latching Current@IG=1.2IGT	I_L	I - III	MAX	90	mA
		II		100	
Critical Rate-of-Rise of Off State Voltage @VD=0.66×VDRM, Tj=150°C, Gate Open	dV/dt	-	MIN	400	V/μs
Peak Forward On-State Voltage @ITM=28A, tp=380μs, Tj=25°C	V_{TM}	-	MAX	1.5	V
Peak Repetitive Forward @VDRM=VRRM, Tj=25°C	I_{DRM}	-	MAX	5	μA
Reverse Blocking Current @VDRM=VRRM, Tj=150°C	I_{RRM}	-	MAX	5	mA

Note: The above typical parameters or typical characteristics are only indicative and do not make specific guarantees. If detailed values are required, additional communication and provision are required.

FIG.1: Maximum power dissipation versus RMS on-state current

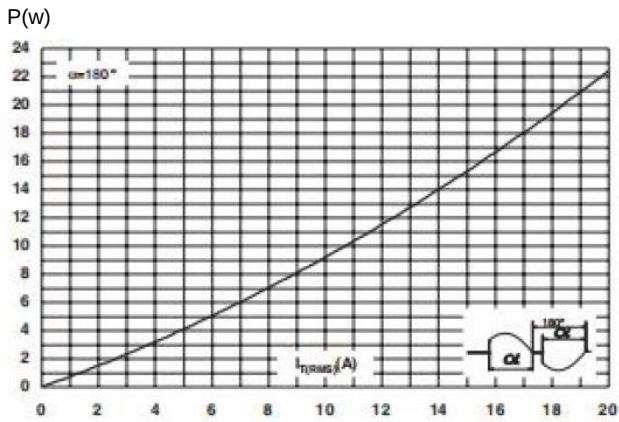


FIG.3: Surge peak on-state current versus number of cycles

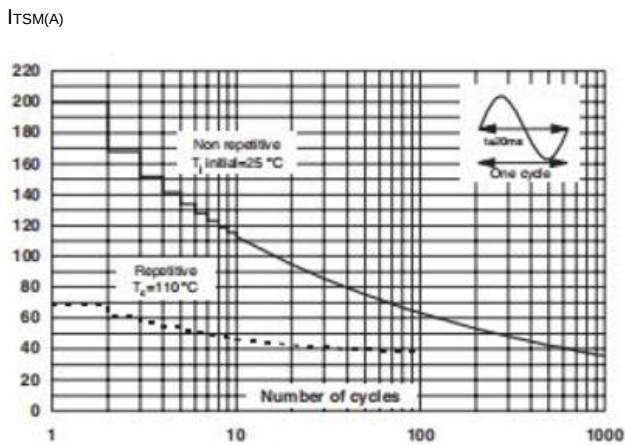


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$, and corresponding value of $I^2 t$

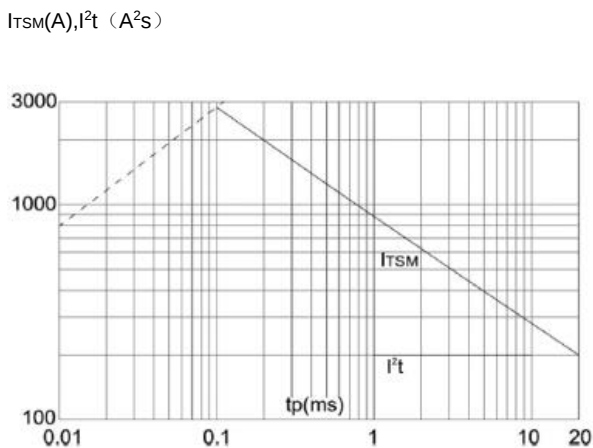


FIG.2: RMS on-state current versus case temperature in different packaging

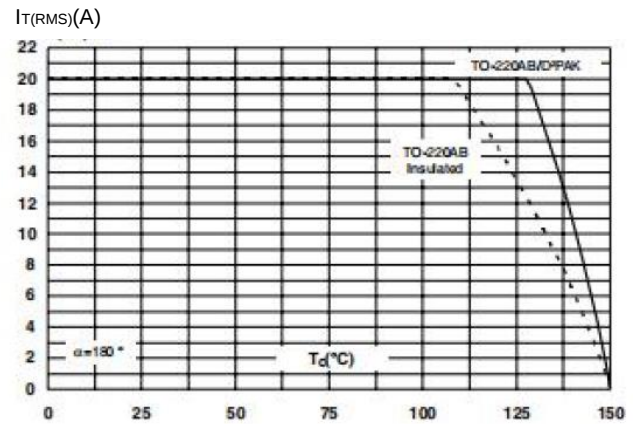


FIG.4: On-state characteristics (maximum values)

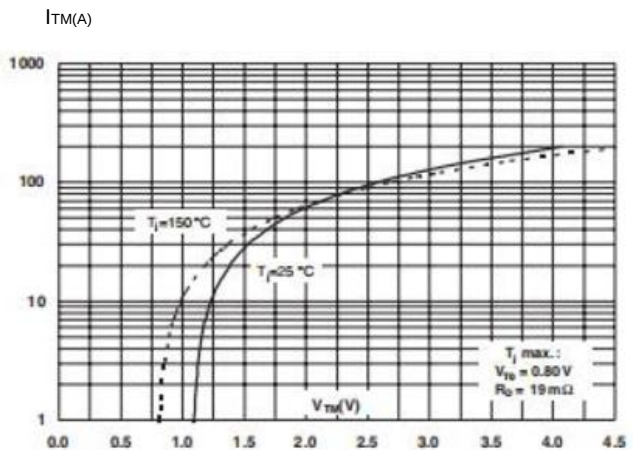
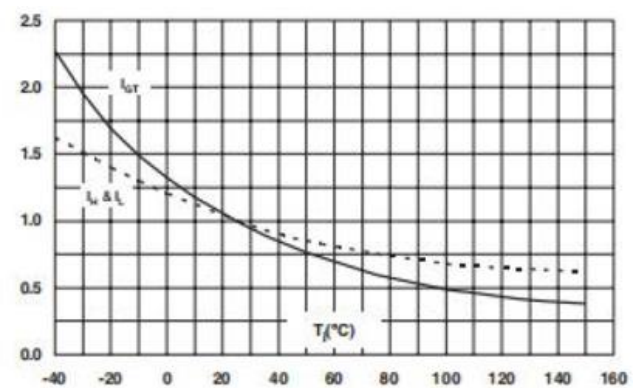
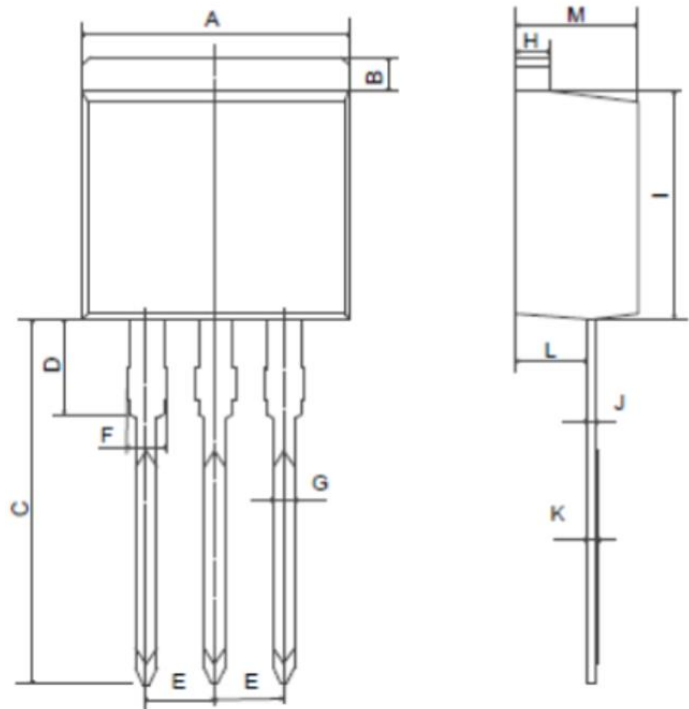


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

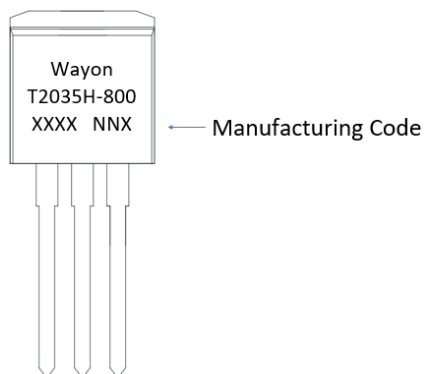
 $I_{GT}, I_H, I_L(T_j) / I_{GT}, I_H, I_L(T_j=25^\circ C)$


Outline Drawing

SYMBOL	MM		
	MIN	NOM	MAX
A	9.86	-	10.40
B	1.01	-	1.50
C	13.01	-	14.16
D	3.30	-	4.00
E	2.39	2.54	2.69
F	1.07	-	1.47
G	0.71	-	0.96
H	1.17	-	1.42
I	8.45	-	9.10
J	0.28	-	0.53
K	0.32	-	0.52
L	2.45	-	2.89
M	4.37	-	4.90



Marking Code



Package Information

Package	Base qty.	Delivery mode
TO-262	50	Tube

Contact Information

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