

T1635H-800NL

Silicon Controlled Rectifier

Features

- Blocking Voltage to 800V
- Glass Passivated Surface for Reliability and Uniformity
- RoHS Compliant & HF
- High Dv/Dt Rate
- I_{T(RMS)} to 16A of Triacs
- High Junction Temperature and High Environment Temperature Condition



Pin Configuration

Absolute Maximum Ratings (Tc=25°C Unless otherwise specified)

Parameter	Symbol	Value	Unit
Storage junction temperature range	Tstg	-40~150	$^{\circ}$
Operating junction temperature range	Tj	-40~150	$^{\circ}$
Repetitive peak off-state voltage (Tj=25°C)	Vdrm	800	V
Repetitive peak reverse voltage (Tj=25°C)	Vrrm	800	V
RMS on-state current	T(RMS)	16	А
Non repetitive surge peak on-state current (full cycle, F=50Hz)	Ітѕм	160	А
I ² t value for fusing (tp=10ms)	l ² t	128	A ² s
Critical rate of rise of on-state current (IG=2×IGT)	dl/dt	50	A/μs
Peak gate current	Ідм	4	А
Average gate power dissipation	PG(AV)	1	W

Peak gate power	Рдм	5	W
Thermal Resistance(between Junction and Case)	$R_{\theta(J-C)}$	1.2 (Тур.)	°C/W

Electronics Characteristics (Tc=25°C Unless otherwise specified)

3 Quadrants:

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Parameter	Symbol	Quadrant		T1635	Unit	
Gate Trigger Current (Continuous dc) @VD=12V, RL=33Ω	Іст	I - II -III	MAX	MAX	35	mA
Gate Trigger Voltage (Continuous dc) @VD=12V, RL=33Ω	Vgт			1.3	V	
Gate non-trigger voltage@VD=VDRM	Vgd	I - II -III	MIN	0.2	V	
Holding Current@IT=100mA	Ін	-	MAX	50	mA	
Latakin a Compani @ IO A GIOT		I -III	MAX	100	mA	
Latching Current@IG=1.2IGT		II		100		
Critical Rate-of-Rise of Off State Voltage @VD=0.66×VDRM, Tj=150℃, Gate Open	dV/dt	-	MIN	1000	V/µs	
Peak Forward On-State Voltage @ITM=22.5A,tp=380μs, Tj=25℃	Vтм	-	MAX	1.55	V	
Peak Repetitive Forward @VDRM=VRRM,Tj=25℃	I DRM	-	MAX	5	μΑ	
Reverse Blocking Current @VDRM=VRRM,Tj=125℃	IRRM	-	MAX	2	mA	

Note: The above typical parameters or typical characteristics are only indicative and do not make specific guarantees. If detailed values are required, additional communication and provision are required.

FIG.1: Maximum power dissipation versus RMS on-state current

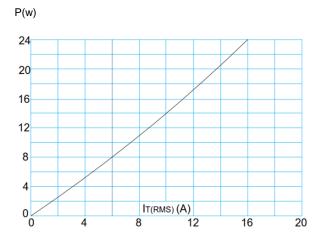


FIG.3: Surge peak on-state current versus number of cycles

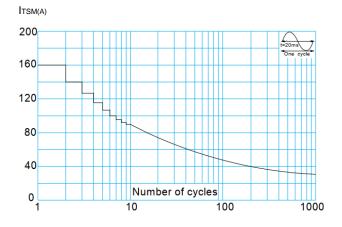


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<10ms, and corresponging value of I^2 t ($I - II - III : dI/dt < 50A/\mu s; IV : dI/dt < 10A/\mu s)$

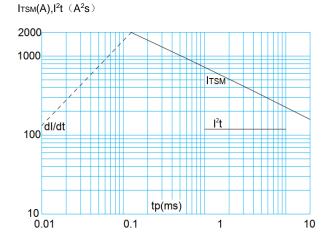


FIG.2: RMS on-state current versus case temperature in different packaging



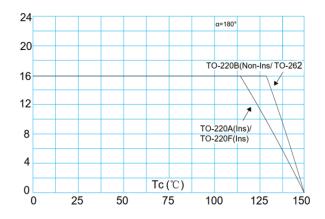


FIG.4:On-state characteristics (maximum values)

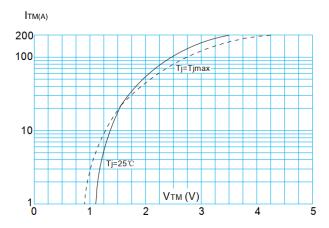
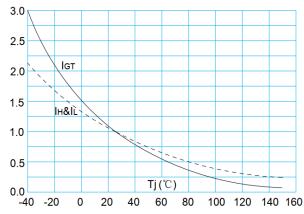


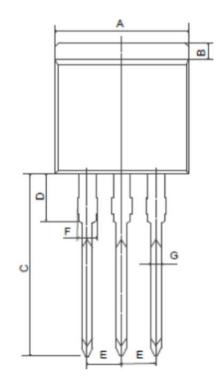
FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

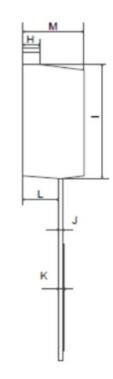




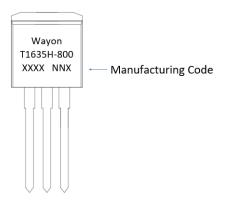
Outline Drawing

SYMBOL			
STIVIBOL	MIN	NOM	MAX
Α	9.86	-	10.40
В	1.01	-	1.50
С	13.01	-	14.16
D	3.30	-	4.00
E	2.39	2.54	2.69
F	1.07	-	1.47
G	0.71	-	0.96
Н	1.17	-	1.42
1	8.45	-	9.10
J	0.28	-	0.53
K	0.32	-	0.52
L	2.45	-	2.89
М	4.37	-	4.90





Marking Code



Package Information

Package	Base qty.	Delivery mode
TO-262	50	Tube

Contact Information

No.1001, Shiwan(7) Road, Pudong District, Shanghai, P.R.China.201207 Tel: 86-21-50310888 Fax: 86-21-50757680 Email: market@way-on.com WAYON website: http://www.way-on.com

For additional information, please contact your local Sales Representative.

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