

T1235H-800A

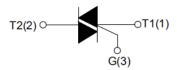
Silicon Controlled Rectifier

Features

- Blocking Voltage to 800V
- Glass Passivated Surface for Reliability and Uniformity
- RoHS Compliant & HF
- High Dv/Dt Rate
- I_{T(RMS)} to 12A of Triacs
- High Junction Temperature and High Environment Temperature Condition



Pin Configuration



Absolute Maximum Ratings (Tc=25°C Unless otherwise specified)

Parameter	Symbol	Value	Unit
Storage junction temperature range	Tstg	-40~150	$^{\circ}$
Operating junction temperature range	Tj	-40~150	$^{\circ}$
Repetitive peak off-state voltage (Tj=25°ℂ)	Vdrm	800	V
Repetitive peak reverse voltage (Tj=25℃)	Vrrm	800	V
RMS on-state current	T(RMS)	12	А
Non repetitive surge peak on-state current (full cycle, F=50Hz)	Ітѕм	120	А
I ² t value for fusing (tp=10ms)	I ² t	72	A ² s
Critical rate of rise of on-state current (IG=2×IGT)	dl/dt	50	A/µs
Peak gate current	Ісм	4	А
Average gate power dissipation	PG(AV)	1	W

Peak gate power	Рдм	5	W
Thermal Resistance(between Junction and Case) @TO-220A(Ins)	R _{θ(J-C)}	2.3 (Typ.)	°C/W

Electronics Characteristics (Tc=25℃ Unless otherwise specified)

3 Quadrants:

Parameter	Symbol	Quadrant		Value	Unit	
raidilletei	Symbol	Quaurant		T1235	Offic	
Gate Trigger Current (Continuous dc) @VD=12V, RL=33Ω	Іст	I - II -III	MAX	MAX	35	mA
Gate Trigger Voltage (Continuous dc) @VD=12V, RL=33Ω	Vgт			1.5	V	
Gate non-trigger voltage@VD=VDRM	VGD	I - II -III	MIN	0.2	V	
Holding Current@IT=100mA	Ін	-	MAX	45	mA	
Lotabing Current@IC=1 2ICT		I -III	MAX	80	mA	
Latching Current@IG=1.2IGT	J L	II		100		
Critical Rate-of-Rise of Off State Voltage @VD=0.66×VDRM, Tj=150°C, Gate Open	dV/dt	-	MIN	400	V/µs	
Peak Forward On-State Voltage @ITM=17A,tp=380µs, Tj=25℃	Vтм	-	MAX	1.5	V	
Peak Repetitive Forward @VDRM=VRRM,Tj=25℃	[DRM	-	MAX	5	μА	
Reverse Blocking Current @VDRM=VRRM,Tj=150℃	İrrm	-	MAX	5.5	mA	

Note: The above typical parameters or typical characteristics are only indicative and do not make specific guarantees. If detailed values are required, additional communication and provision are required.

FIG.1: Maximum power dissipation versus RMS on-state current

P(w)

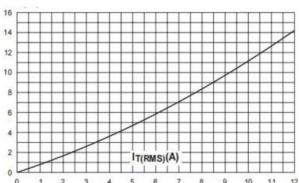


FIG.3: Surge peak on-state current versus number of cycles

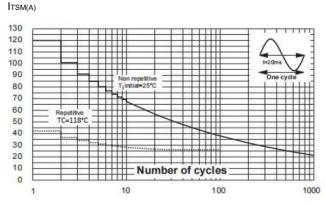


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<20ms, and corresponging value of I2 t

 $\mathsf{ITSM}(\mathsf{A}), \mathsf{I}^2t \ (\mathsf{A}^2\mathsf{s}\,)$

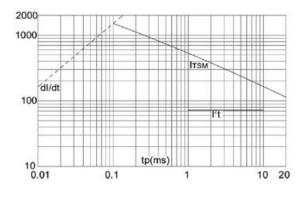


FIG.2: RMS on-state current versus case temperature in different packaging

IT(RMS)(A)

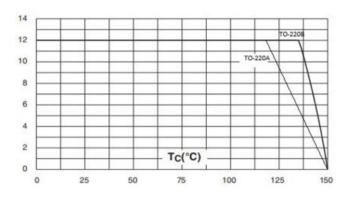


FIG.4:On-state characteristics (maximum values)

I_{TM(A)}

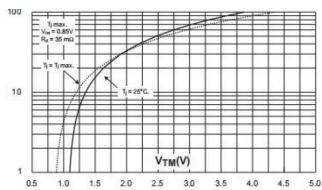
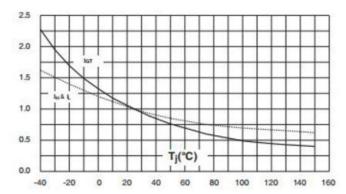


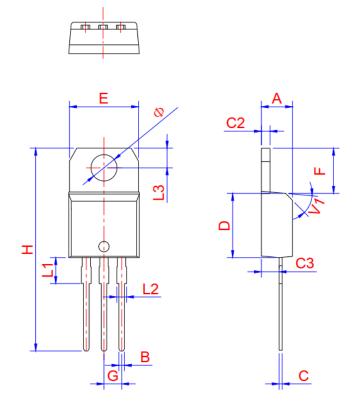
FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

Ідт,Ін,ІL(Tj)/Ідт,Ін,І $L(Tj=25^{\circ}C)$

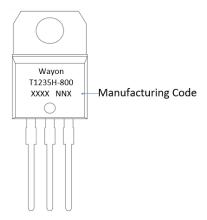


Outline Drawing- TO-220A Ins

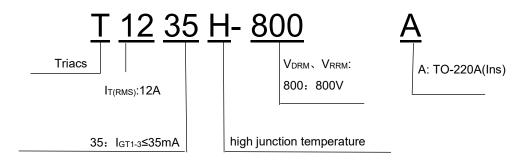
Oddinio L	<u> </u>	10 2207			
SYMBOL	MM				
STIVIDOL	MIN	NOM	MAX		
Α	4.20	4.47	4.60		
В	0.61	-	0.93		
С	0.40	0.50	0.70		
C2	1.20	1.27	1.48		
C3	2.20	-	2.75		
D	8.60	-	9.90		
Е	9.70	-	10.60		
F	6.00	-	7.15		
G	-	2.54	-		
Н	28	-	29.8		
L1	-	3.50	-		
L2	1.10	-	1.70		
L3	2.55	-	2.95		
V1	-	45°	-		
Ф	3.65	3.75	3.85		



Marking Code



Part Number System



Package Information

Package	Base qty.	Delivery mode
TO-220A(Ins)	50	Tube

Contact Information

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6 / 6