

24V Common-Drain Dual N-Channel MOSFET

Description

WMAC62025A uses advanced power trench technology that has been especially tailored to minimize the on-state resistance. This device is suitable for un-directional or bidirectional load switch, facilitated by its common-drain configuration

$V_{SSS}(V)$	$I_S(A)$	$R_{SS(on)}TYP (m\Omega)$
24	30	2.4 @ $V_{GS}=4.5V$
		2.5 @ $V_{GS}=3.8V$
		2.8 @ $V_{GS}=3.1V$
		3.4 @ $V_{GS}=2.5V$

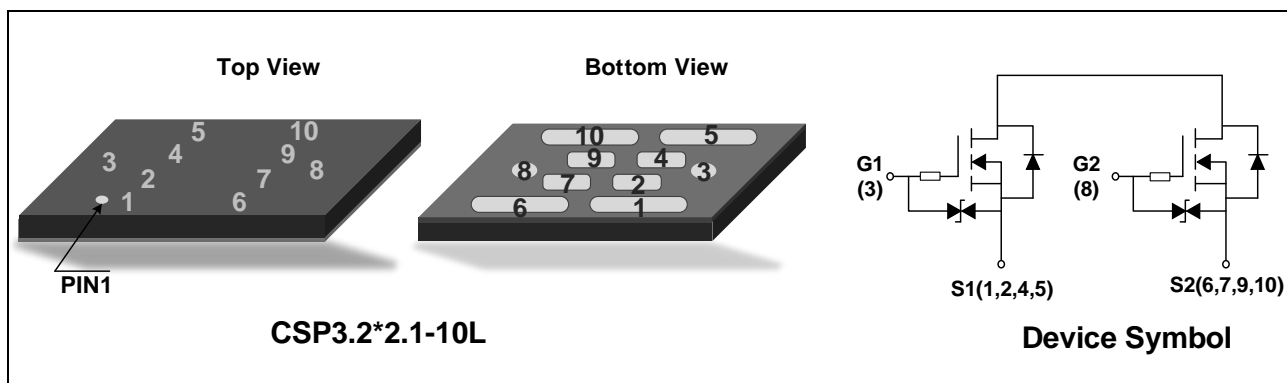
Features

- CSP(Chip Size Package)
- Super High Dense Cell for Low $R_{SS(ON)}$
- RoHS Compliant and Halogen-Free
- ESD Protected

Applications

- Battery Protection
- Load Switch

Schematic & PIN Configuration



Absolute Maximum Rating ($T_A=25^{\circ}C$ unless otherwise noted)

Parameter		Symbol	Value	Unit
Source -Source Voltage		V_{SSS}	24	V
Gate-Source Voltage		V_{GSS}	± 12	V
Continuous Source Current ¹	$T_A = 25^{\circ}C$	I_S	30	A
Pulsed Source Current ²		I_{SP}	120	A
Total Power Dissipation ¹	$T_A = 25^{\circ}C$	P_D	3.1	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^{\circ}C$

Thermal Characteristics

Maximum Junction-to-Ambient	$R_{\theta JA1}$	40	$^{\circ}C/W$
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Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Source-Source Breakdown Voltage	V _{(BR)SSS}	V _{GS} = 0V, I _S = 500μA	24	-	-	V
Zero Gate Voltage Source Current	I _{SSS}	V _{SS} = 24V, V _{GS} = 0V	-	-	1	μA
Gate-Source Leakage Current	I _{GSS}	V _{SS} = 0V, V _{GS} = ±5V	-	-	±1	μA
		V _{SS} = 0V, V _{GS} = ±12V	-	-	±10	
Gate-Threshold Voltage	V _{GS(th)}	V _{SS} = V _{GS} , I _S = 250μA	0.4	0.8	1.3	V
Source-Source on-Resistance	R _{SS(on)}	V _{GS} = 4.5V, I _S = 6A	1.8	2.4	3.1	mΩ
		V _{GS} = 3.8V, I _S = 6A	1.9	2.5	3.5	
		V _{GS} = 3.1V, I _S = 6A	2.0	2.8	4.1	
		V _{GS} = 2.5V, I _S = 6A	2.4	3.4	5	
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{SS} = 12V, V _{GS} =0V, f =1MHz	-	4700	-	pF
Output Capacitance	C _{oss}		-	470	-	
Reverse Transfer Capacitance	C _{rss}		-	395	-	
Gate Resistance	R _g	f = 1MHz	-	1.1	-	KΩ
Switching Characteristics						
Total Gate Charge	Q _g	V _{GS} = 4.5V, V _{SS} = 12V, I _S = 6A	-	34.5	-	nC
Gate-Source Charge	Q _{gs}		-	7.8	-	
Gate-Drain Charge	Q _{gd}		-	14.2	-	
Turn-on Delay Time	t _{d(on)}	V _{GS} = 4.5V, V _{SS} = 12V, I _S = 6A, R _G = 3Ω	-	1.5	-	μs
Rise Time	t _r		-	3.9	-	
Turn-off Delay Time	t _{d(off)}		-	3.5	-	
Fall Time	t _f		-	10.8	-	
Source-Source Diode Characteristics						
Forward Source to Source Voltage	V _{F(S-S)}	I _S = 1A, V _{GS} = 0V	-	0.65	1.2	V

Notes:

1. Mounted on Ceramic substrate (70mm x 70mm x t1.0mm)
2. $PW < 10\mu s$, duty cycle $\leq 1\%$.

Typical Characteristics

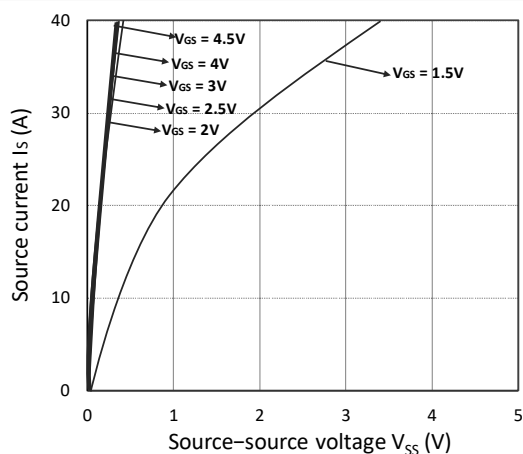


Figure 1. Output Characteristics

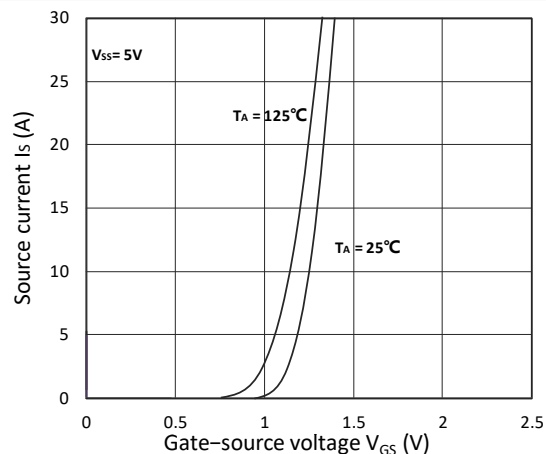


Figure 2. Transfer Characteristics

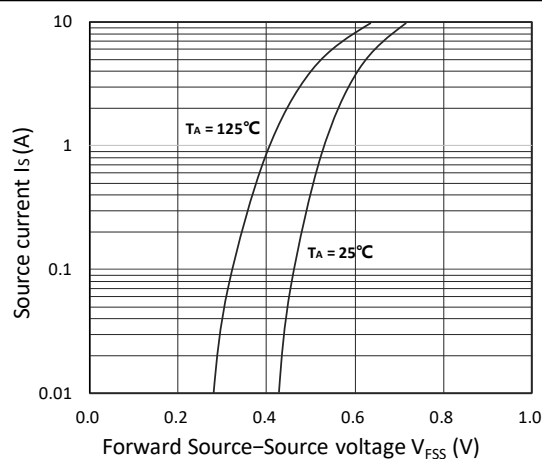
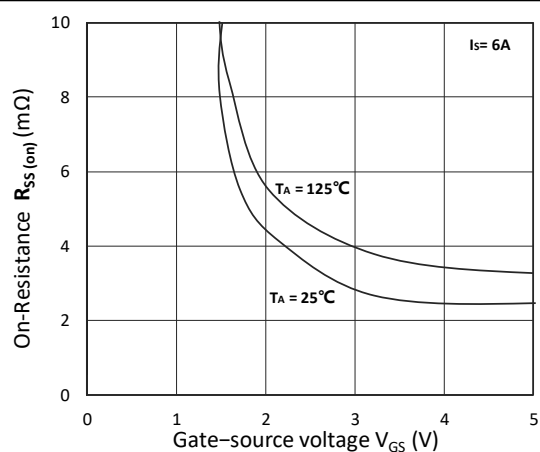
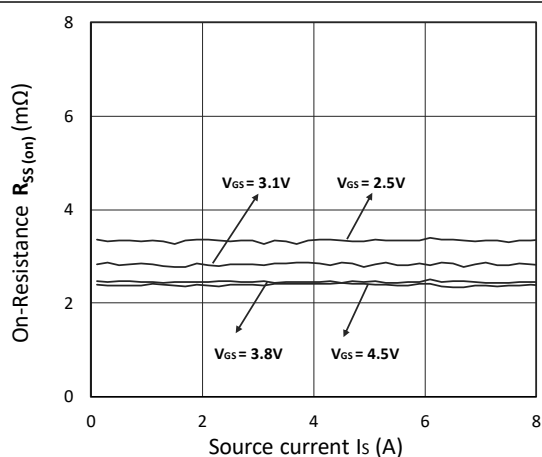
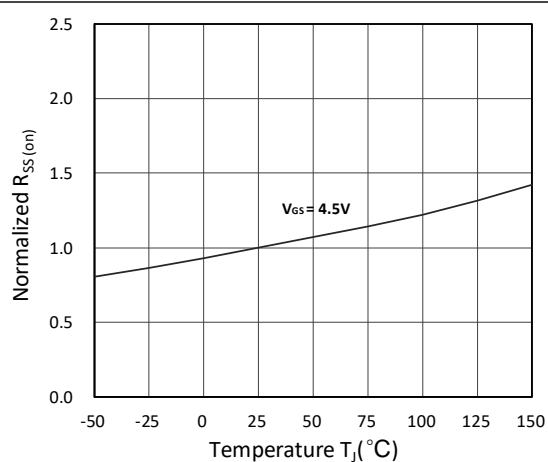


Figure 3. Forward Characteristics of Reverse

Figure 4. $R_{SS(ON)}$ vs. V_{GS} Figure 5. $R_{SS(ON)}$ vs. I_S Figure 6. Normalized $R_{SS(ON)}$ vs. Temperature

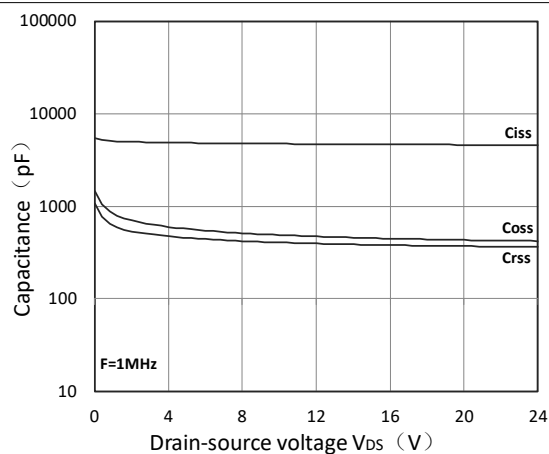


Figure 7. Capacitance Characteristics

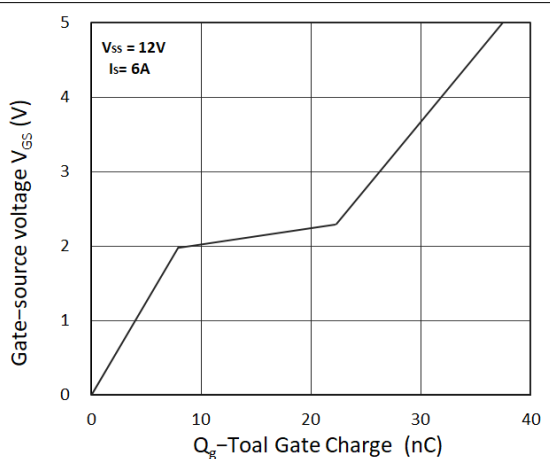
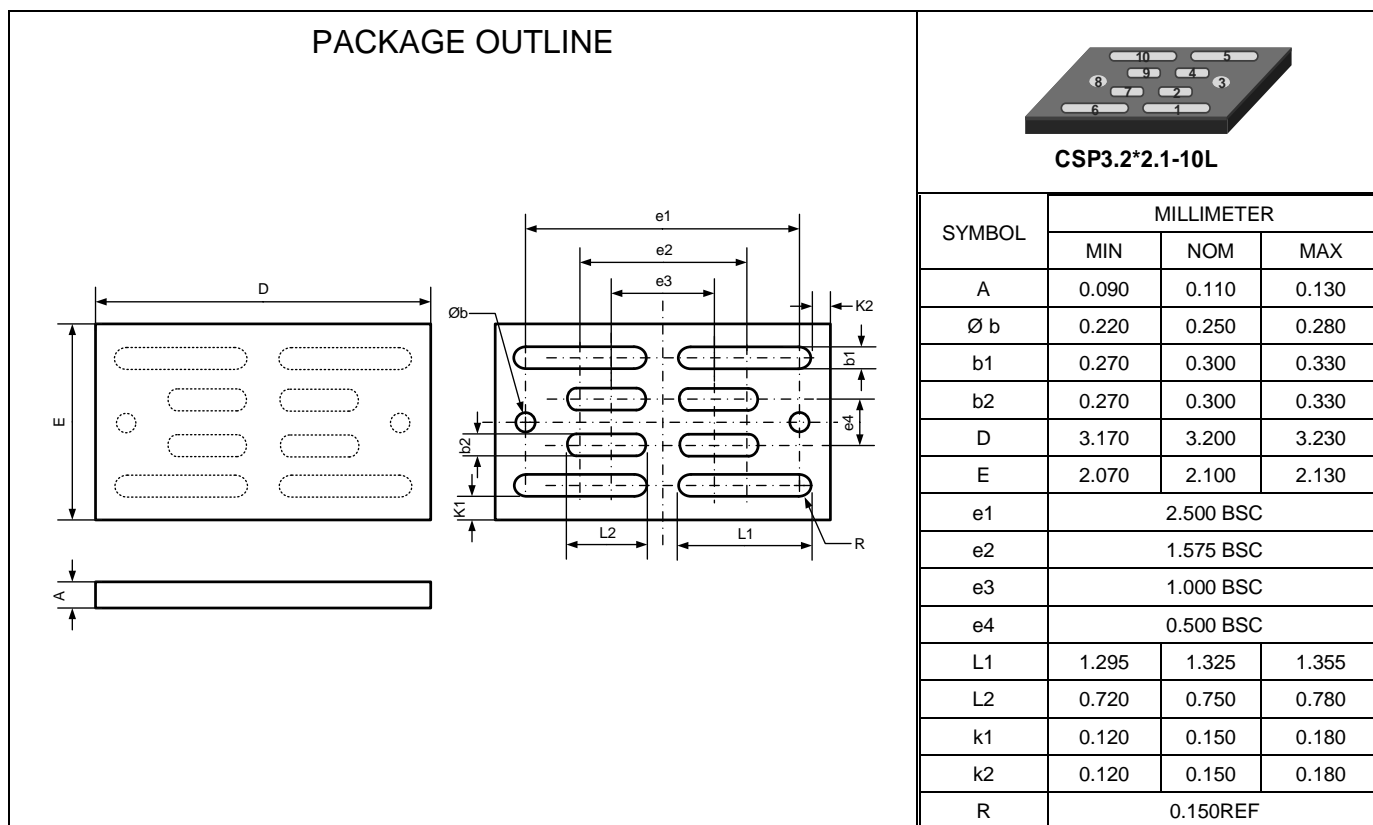


Figure 8. Gate Charge Characteristics

Outline Drawing CSP3.2*2.1-10L



Marking Codes

Part Number	WMAC62025A	
Marking Code	<div style="border: 1px solid black; padding: 5px; display: inline-block;"> 62025 XXXXX ● </div>	62025= Device code XXXXX= Date code

Package Information

Qty: 5k/Reel

CONTACT INFORMATION

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