

100V Dual N-Channel Enhancement Mode Power MOSFET

Description

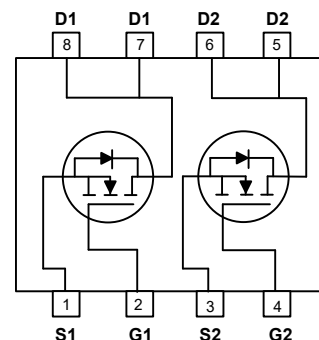
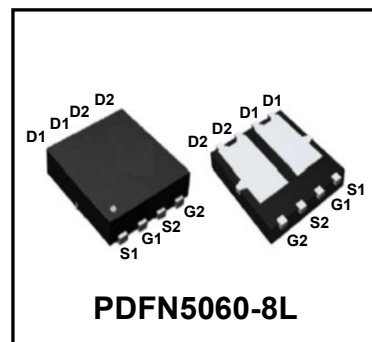
WMB12DN10TS uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Features

- $V_{DS} = 100V$, $I_D = 11A$
Typ. $R_{DS(on)} = 100m\Omega$ @ $V_{GS} = 10V$
Typ. $R_{DS(on)} = 108m\Omega$ @ $V_{GS} = 4.5V$
- Green Device Available
- 100% EAS Guaranteed
- RoHS Compliant & Halogen-Free

Applications

- Synchronous Rectification
- Motor Driver

Absolute Maximum Ratings ($T_A = 25^\circ C$, unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source voltage		V_{DS}	100	V
Gate-Source voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_C = 25^\circ C$	I_D	11	A
	$T_C = 100^\circ C$		6.6	
Pulsed Drain Current ¹		I_{DM}	42	A
Single Pulse Avalanche Energy ²		EAS	9.8	mJ
Total Power Dissipation	$T_C = 25^\circ C$	P_D	25	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ³	$R_{\theta JA}$	60	$^\circ C/W$
Thermal Resistance from Junction-to-Case	$R_{\theta JC}$	5	$^\circ C/W$

Electrical Characteristics ($T_J = 25^\circ\text{C}$, unless otherwise noted)

Parameter		Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics							
Drain-Source Breakdown Voltage		V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	100	-	-	V
Gate-body Leakage Current		I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T _J =25°C	I _{DSS}	V _{DS} = 100V, V _{GS} = 0V	-	-	1	μA
	T _J =100°C			-	-	100	
Gate-Threshold Voltage		V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1	1.6	2.2	V
Drain-Source On-Resistance ⁴		R _{DS(on)}	V _{GS} = 10V, I _D = 6A	-	100	125	mΩ
			V _{GS} = 4.5V, I _D = 4A		108	140	
Forward Transconductance ⁴		g _{fs}	V _{DS} = 10V, I _D = 6A	-	12	-	S
Dynamic Characteristics ⁵							
Input Capacitance		C _{iss}	V _{DS} = 50V, V _{GS} =0V, f =1MHz	-	700	-	pF
Output Capacitance		C _{oss}		-	25	-	
Reverse Transfer Capacitance		C _{rss}		-	20	-	
Gate Resistance		R _G	f =1MHz	-	1.2	-	Ω
Switching Characteristics ⁵							
Total Gate Charge		Q _g	V _{GS} = 10V, V _{DS} = 50V, I _D = 6A	-	13.9	-	nC
Gate-Source Charge		Q _{gs}		-	2.5	-	
Gate-Drain Charge		Q _{gd}		-	2.3	-	
Turn-on Delay Time		t _{d(on)}	V _{GS} = 10V, V _{DD} = 50V, R _G = 3Ω, I _D = 6A	-	6.3	-	ns
Rise Time		t _r		-	1.7	-	
Turn-off Delay Time		t _{d(off)}		-	15.8	-	
Fall Time		t _f		-	2.1	-	
Body Diode Reverse Recovery Time		t _{rr}	I _F = 6A, di/dt = 100A/μs	-	30	-	ns
Body Diode Reverse Recovery Charge		Q _{rr}		-	24	-	nC
Drain-Source Body Diode Characteristics							
Diode Forward Voltage ⁴		V _{SD}	I _S = 6A, V _{GS} = 0V	-	-	1.2	V
Continuous Source Current	T _C =25°C	I _S	-	-	-	11	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)} = 150^\circ\text{C}$
2. The test condition is $V_{DD} = 25V, V_{GS} = 10V, L = 0.4mH, I_{AS} = 21A$
3. The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
5. This value is guaranteed by design hence it is not included in the production test.

Typical Characteristics

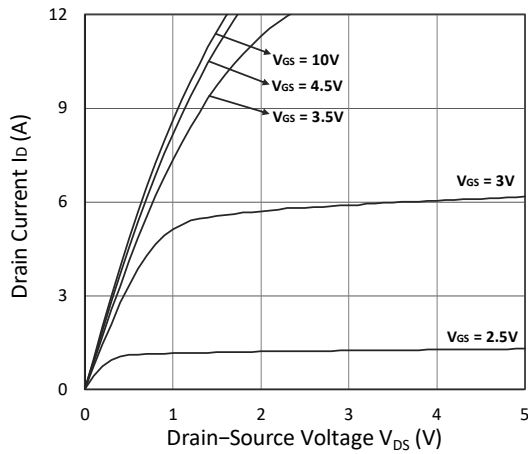


Figure 1. Output Characteristics

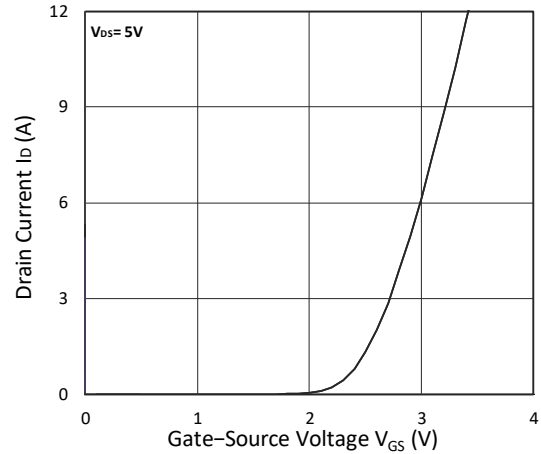


Figure 2. Transfer Characteristics

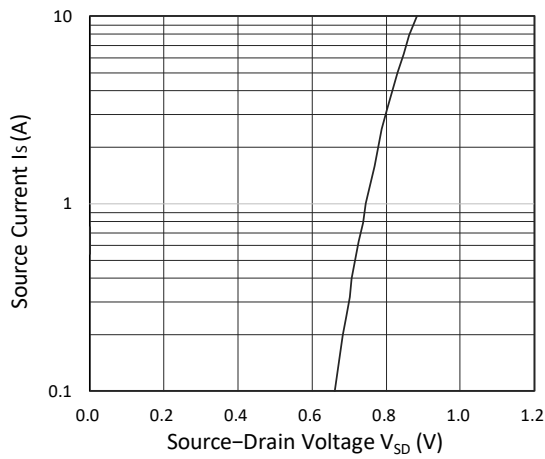
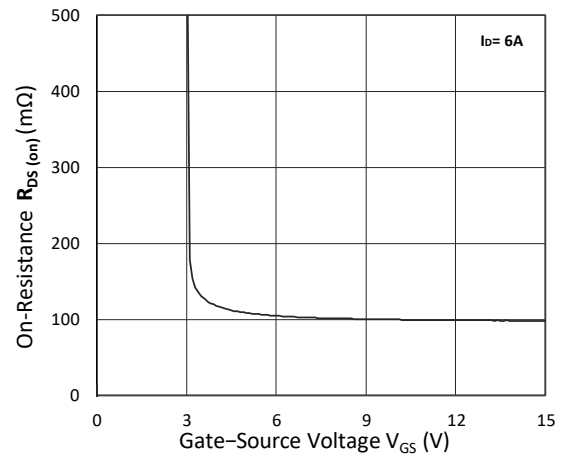
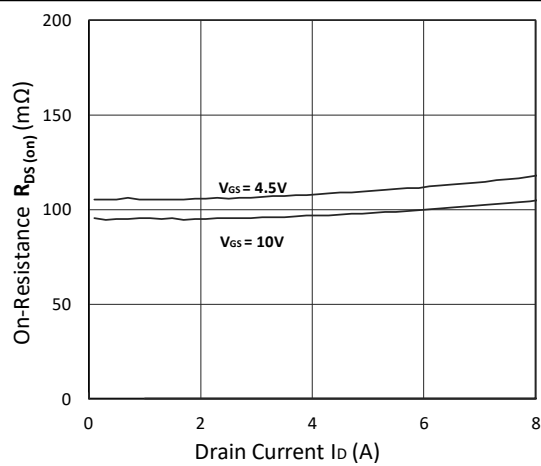
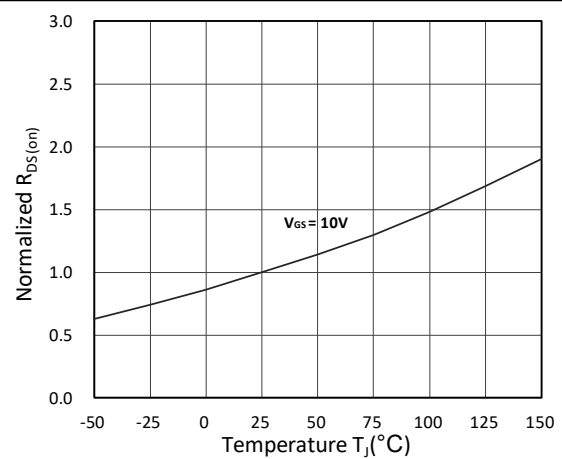


Figure 3. Forward Characteristics of Reverse

Figure 4. $R_{DS(on)}$ vs. V_{GS} Figure 5. $R_{DS(on)}$ vs. I_D Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

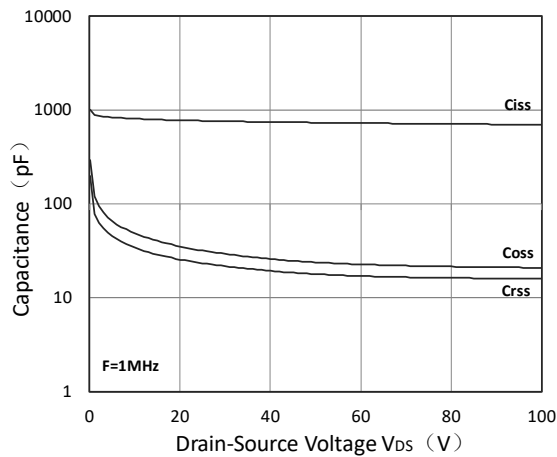


Figure 7. Capacitance Characteristics

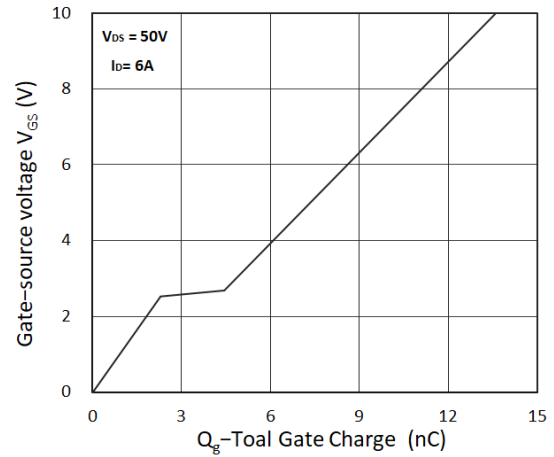


Figure 8. Gate Charge Characteristics

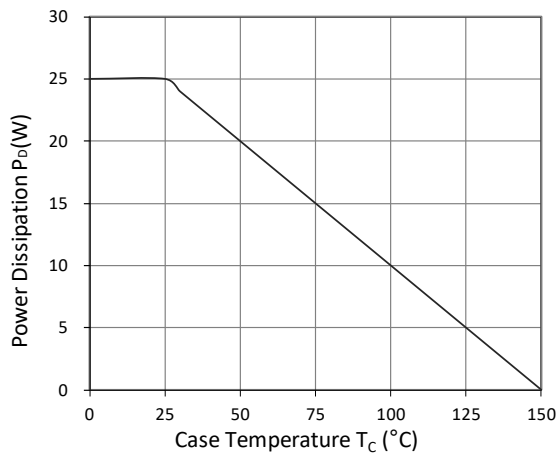


Figure 9. Power Dissipation

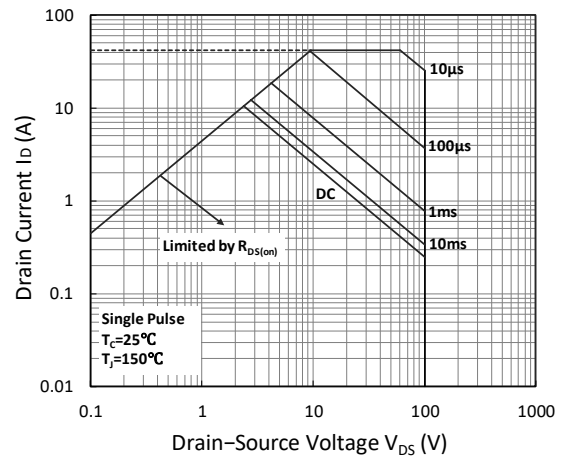


Figure 10. Safe Operating Area

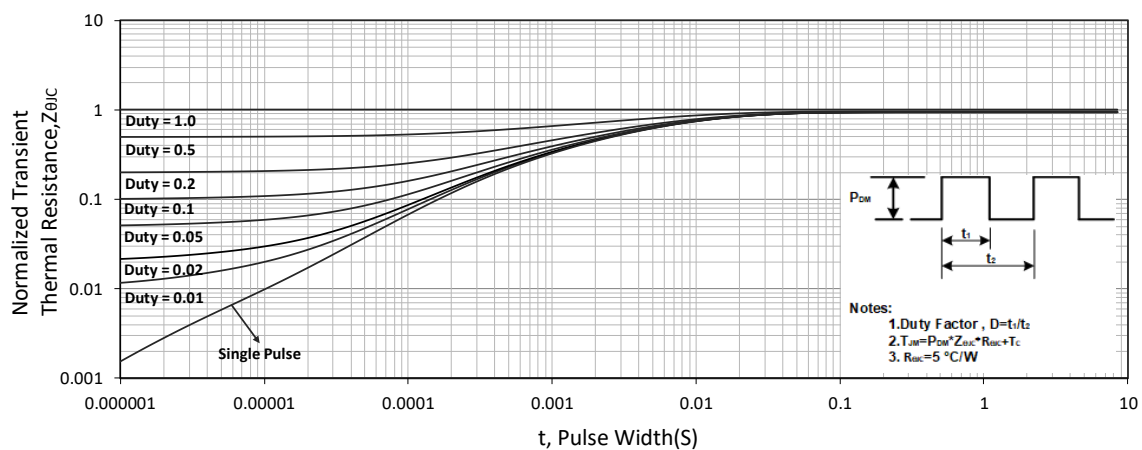


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit

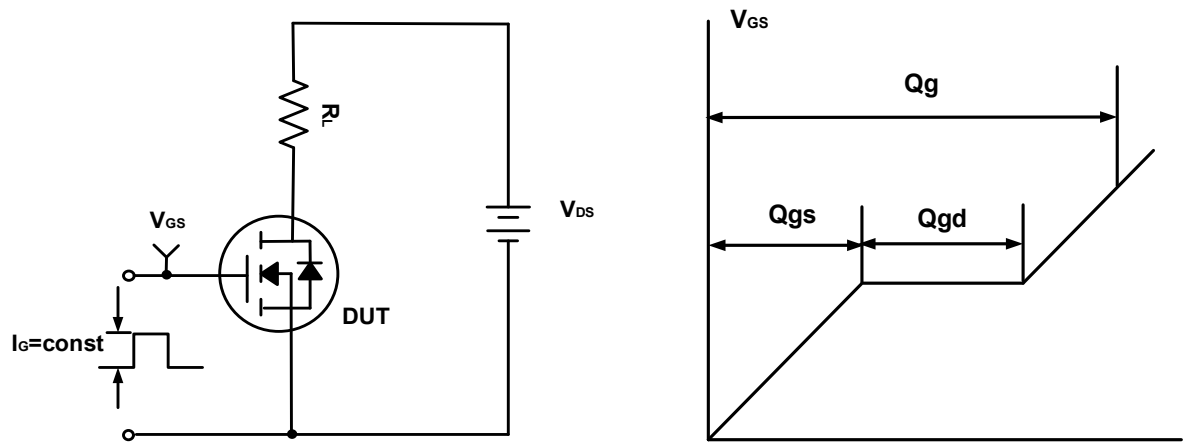


Figure A. Gate Charge Test Circuit & Waveforms

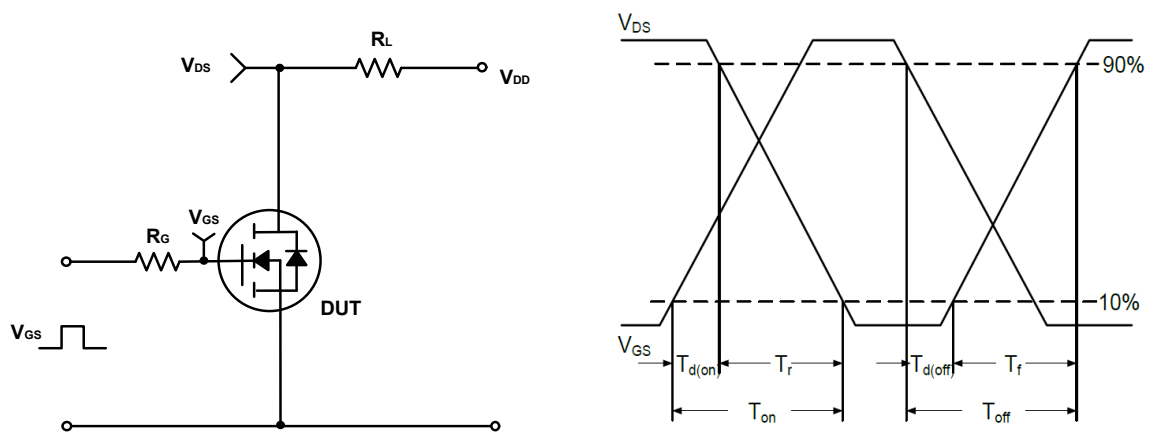


Figure B. Switching Test Circuit & Waveforms

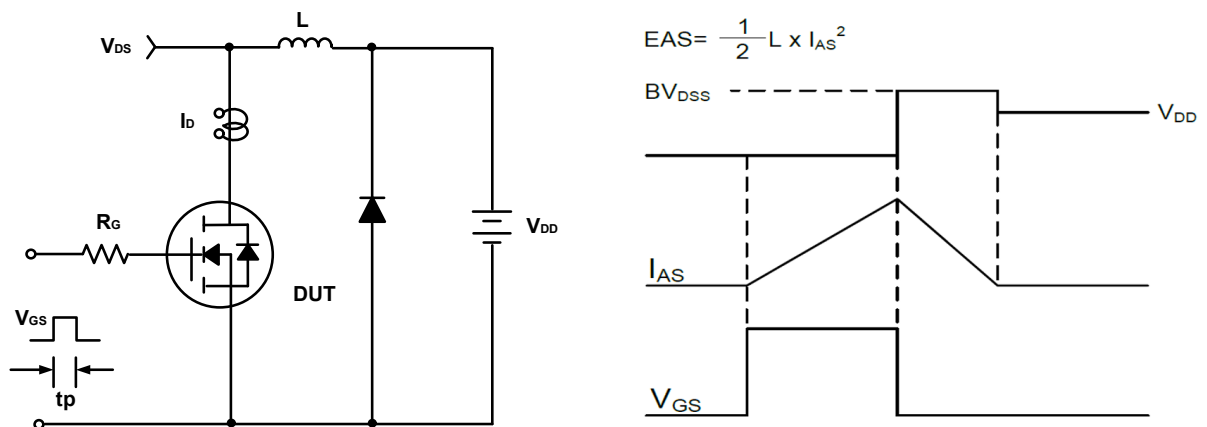
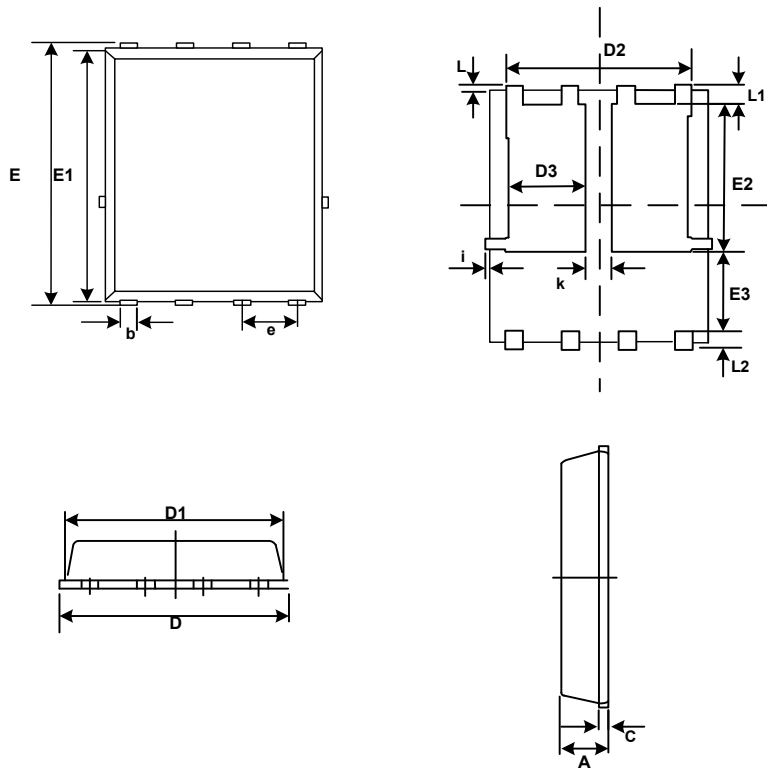


Figure C. Unclamped Inductive Switching Circuit & Waveforms

Mechanical Dimensions for PDFN5060-8L



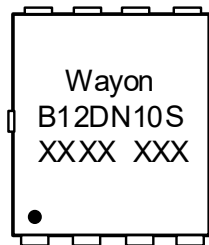
COMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	0.90	1.17
b	0.33	0.51
c	0.20BSC	
D	4.80	5.40
D1	4.80	5.00
D2	3.95	4.45
D3	1.55	1.80
E	5.90	6.15
E1	5.65	5.85
E2	3.30	3.78
E3	1.10	/
e	1.27BSC	
L	0.05	0.25
L1	0.38	0.61
L2	0.38	0.71
i	/	0.18
k	0.50	0.70

Ordering Information

Part	Package	Marking	Packing method
WMB12DN10TS	PDFN5060-8L	B12DN10S	Tape and Reel

Marking Information



B12DN10S = Device code

XXXX XXX= Date code

Contact Information

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WAYON website: <http://www.way-on.com>

For additional information, please contact your local Sales Representative.

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2. The product parameters described in the product specification are numerical values, characteristics, and functions obtained through actual testing or theoretical calculations of the product in an independent or ideal state. Due to the complexity of product applications and variations in test conditions and equipment, there may be slight fluctuations in parameter test values. WAYON shall not guarantee that the actual performance of the product when installed in the customer's system or equipment will be entirely consistent with the product specification, especially concerning dynamic parameters. It is recommended that users consult with professionals for product selection and system design. Users should also thoroughly validate and assess whether the actual parameters and performance when installed in their respective systems or equipment meet their requirements or expectations. Additionally, users should exercise caution in verifying product compatibility issues, and WAYON assumes no responsibility for the application of the product.
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