

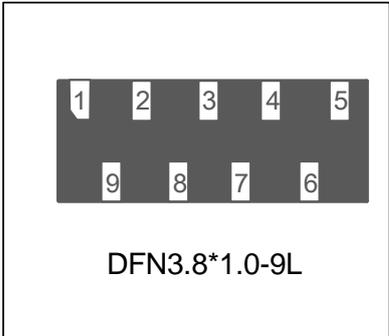


# WE3.3-8R2P

## Transient Voltage Suppressor

### Features

- 63 Watts peak pulse power ( $t_p=8/20\mu s$ )
- Protects 8 high-speed IO channels
- Low capacitance: 0.3pF typical
- Low leakage current
- Low operating and clamping voltage
- Solid-state silicon-avalanche TVS process technology



### IEC COMPATIBILITY (EN61000-4)

- IEC 61000-4-2 (ESD)  $\pm 20kV$  (air),  $\pm 20kV$  (contact)
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 4.5A (8/20 $\mu s$ )

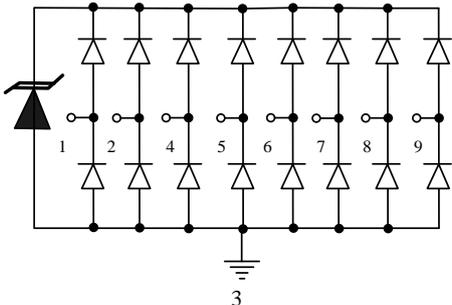
### Mechanical Characteristics

- JEDEC DFN3.8\*1.0-9L package
- Marking: Marking Code
- Packaging: Tape and Reel
- RoHS Compliant

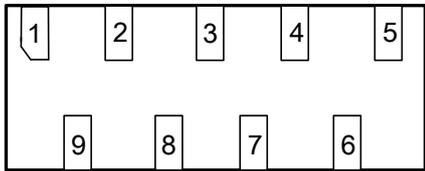
### Applications

- High Definition Multi-Media Interface(HDMI)
- DisplayPort interface
- SATA and eSATA interface
- 10/100,1000M Ethernet
- V-By-One
- LVDS interfaces

### Circuit Diagram



### Schematic & PIN Configuration

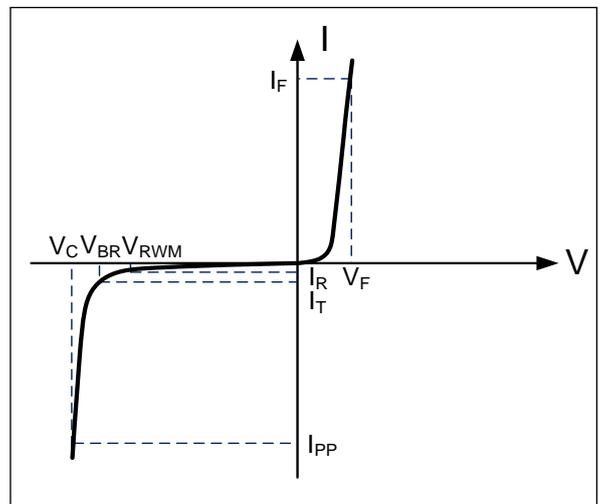


Pin	Identificaion
1,2,4,5,6,7,8,9	I/O
3	Ground

Absolute Maximum Rating			
Rating	Symbol	Value	Units
Peak Pulse Power ( $t_p=8/20\mu s$ ) see Figure1 & Figure2	$P_{PP}$	63	Watts
Peak Pulse Current ( $t_p=8/20\mu s$ )	$I_{PP}$	4.5	A
Operating Temperature	$T_J$	-55 to + 125	°C
Storage Temperature	$T_{STG}$	-55 to +150	°C

Electrical Parameters (T=25°C)

Symbol	Parameter
$I_{PP}$	Reverse Peak Pulse Current
$V_C$	Clamping Voltage @ $I_{PP}$
$V_{RWM}$	Reverse Stand-Off Voltage
$I_R$	Reverse Leakage Current @ $V_{RWM}$
$V_{BR}$	Breakdown Voltage @ $I_T$
$I_T$	Test Current
$I_F$	Forward Current
$V_F$	Forward Voltage @ $I_F$



Electrical Characteristics

WE3.3-8R2P						
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	$V_{RWM}$				3.3	V
Breakdown Voltage	$V_{BR}$	$I_T=1mA$	3.7			V
Reverse Leakage Current	$I_R$	$V_{RWM}=3.3V, T=25^\circ C$			500	nA
Forward Voltage	$V_F$	$I_F=10mA$	0.5		1.2	V
Clamping Voltage	$V_C$	$I_{PP}=1A, t_p=8/20\mu s$		6.8		V
Clamping Voltage	$V_C$	$I_{PP}=4.5A, t_p=8/20\mu s$		10	14	V
Dynamic Resistance <sup>1,2</sup>	$R_{DYN}$	TLP=0.2/100ns		0.33		$\Omega$
ESD Clamping Voltage <sup>1</sup>	$V_C$	$I_{PP} = 4A,$ $t_p = 0.2/100ns$ (TLP)		7.6		V
ESD Clamping Voltage <sup>1</sup>	$V_C$	$I_{PP} = 16A,$ $t_p = 0.2/100ns$ (TLP)		11.5		V
Junction Capacitance	$C_j$	Between I/O pins and Ground $V_R=0V, f=1MHz$		0.6	0.8	pF
		Between I/O pins $V_R=0V, f=1MHz$		0.3	0.4	pF

Notes: 1、 TLP Setting :  $t_p=100ns, t_r=0.2ns, I_{TLP}$  and  $V_{TLP}$  sample window: $t_1=70ns$  to  $t_2=90ns$ .

2、 Dynamic resistance calculated from  $I_{PP}=4A$  to  $I_{PP}=16A$  using "Best Fit".

Typical Characteristics

Figure 1: Peak Pulse Power vs. Pulse Time

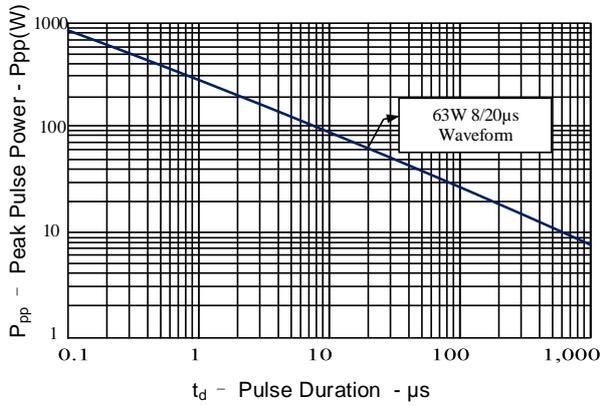


Figure 2: Power Derating Curve

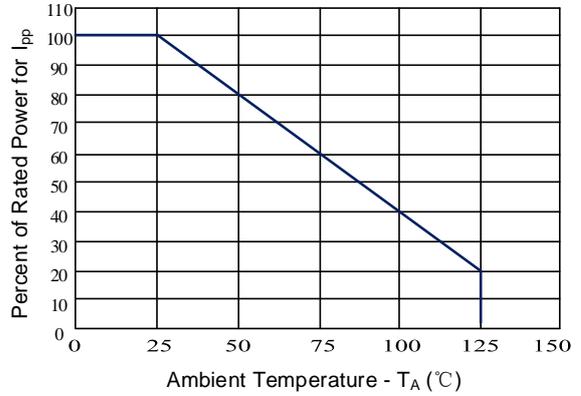


Figure 3: Clamping Voltage vs. Peak Pulse Current

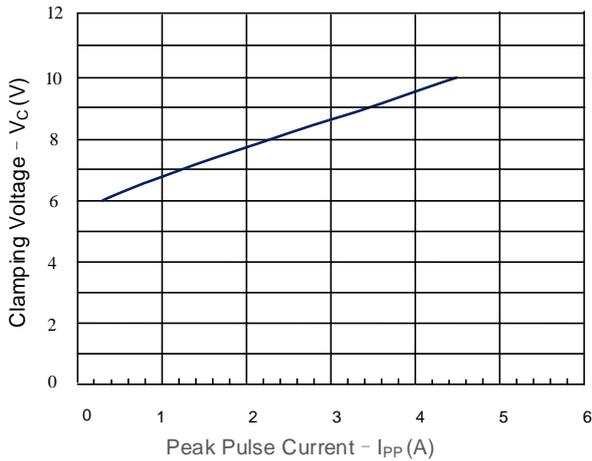


Figure 4: Normalized Junction Capacitance vs. Reverse Voltage

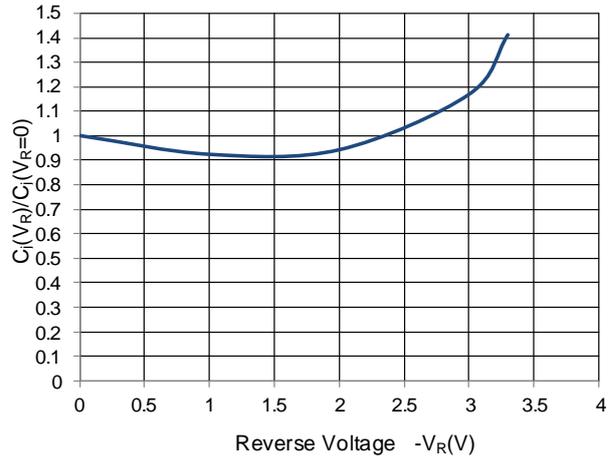


Figure 5: 8/20μs Pulse Waveform

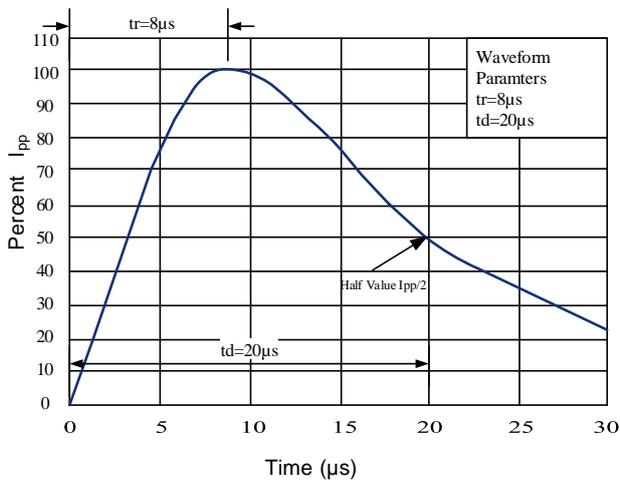
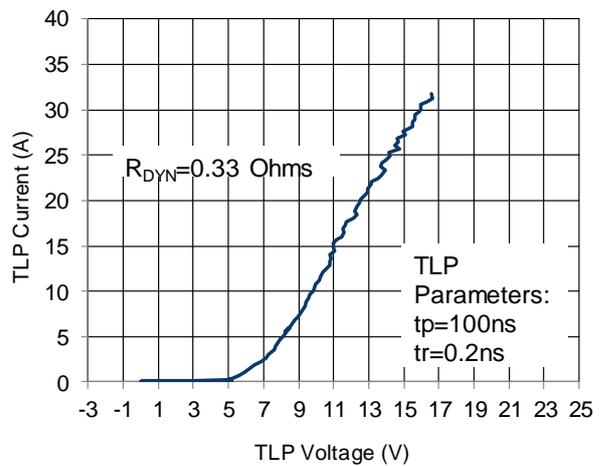
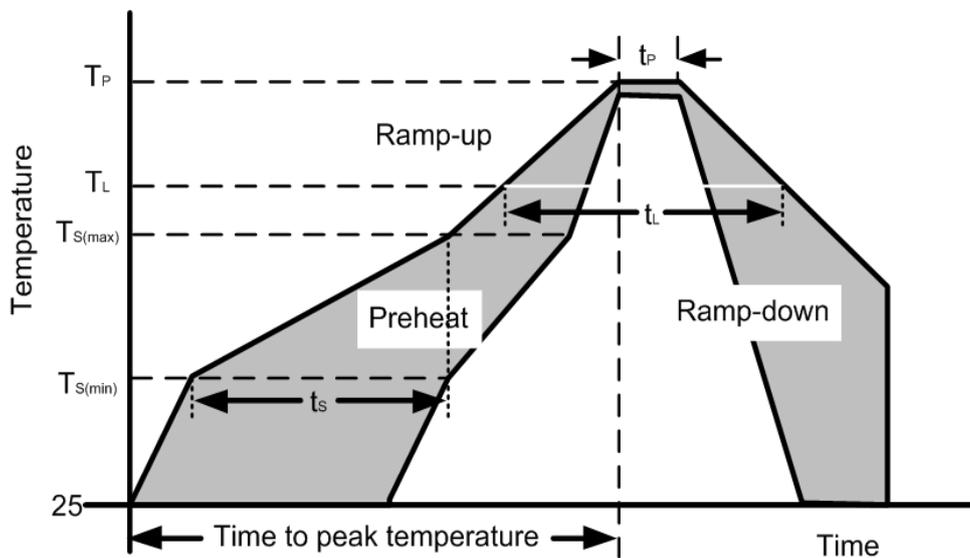


Figure 6: TLP I-V Curve



Soldering Parameters

Reflow Condition		Pb – Free assembly
Pre Heat	Temperature Min ( $T_{s(min)}$ )	150°C
	Temperature Max ( $T_{s(max)}$ )	200°C
	Time (min to max) ( $t_s$ )	60 – 190 secs
Average ramp up rate (Liquidus Temp) ( $T_L$ ) to peak		5°C/second max
$T_{s(max)}$ to $T_L$ —Ramp-up Rate		5°C/second max
Reflow	Temperature ( $T_L$ ) (Liquidus)	217°C
	Temperature ( $t_L$ )	60 – 150 seconds
Peak Temperature ( $T_P$ )		260+0/-5 °C
Time within actual peak Temperature ( $t_p$ )		20 – 40 seconds
Ramp-down Rate		5°C/second max
Time 25°C to peak Temperature ( $T_P$ )		8 minutes Max.
Do not exceed		280°C



Outline Drawing – DFN3.8\*1.0-9L

**NOTES:**

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

**DFN3.8\*1.0-9L**

DIMENSIONS			
DIM	MILLIMETERS		
	MIN	NOM	MAX
D	3.70	3.80	3.90
E	0.90	1.00	1.10
A	0.45	0.50	0.55
A1	0.00	0.02	0.05
A2	0.10	0.15	0.20
b	0.15	0.20	0.25
e	0.80BSC		
e1	0.90BSC		
L	0.20	0.30	0.40

DIMENSIONS		
DIM	INCHES	MILLIMETERS
P	0.031	0.80
P1	0.035	0.90
d	0.012	0.30
Y	0.024	0.60
Y1	0.061	1.55

**NOTES:**

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY.

CONSULT YOUR MANUFACTURING TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

Marking Codes

Part Number	WE3.3-8R2P
Marking Code	.8R2P

Package Information

Qty: 3k/Reel

CONTACT INFORMATION

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For additional information, please contact your local Sales Representative.

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Specifications are subject to change without notice.  
The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time.  
Users should verify actual device performance in their specific applications.