

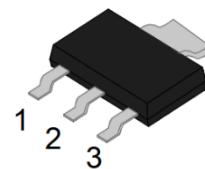


2P4M-T

Silicon Controlled Rectifier

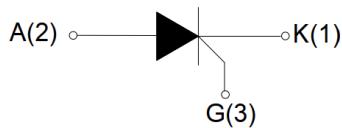
## Features

- Blocking Voltage to 800 V
- Glass Passivated Surface for Reliability and Uniformity
- RoHS Compliant & HF
- High Dv/Dt Rate
- $I_{T(RMS)}$  to 2A of SCR



SOT-223-3L

## Pin Configuration



## Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$ Unless otherwise specified)

Parameter	Symbol	Value	Unit
Storage junction temperature range	$T_{stg}$	-40~150	°C
Operating junction temperature range	$T_j$	-40~110	°C
Repetitive peak off-state voltage ( $T_j=25^\circ\text{C}$ )	$V_{DRM}$	800	V
Repetitive peak reverse voltage ( $T_j=25^\circ\text{C}$ )	$V_{RRM}$	800	V
RMS on-state current	$I_{T(RMS)}$	2	A
Non repetitive surge peak on-state current (full cycle, $F=50\text{Hz}$ )	$I_{TSM}$	20	A
$I^2t$ value for fusing ( $t_p=10\text{ms}$ )	$I^2t$	2	$\text{A}^2\text{s}$
Critical rate of rise of on-state current ( $IG=2\times IGT$ )	$dI/dt$	50	$\text{A}/\mu\text{s}$
Peak gate current	$I_{GM}$	0.2	A
Average gate power dissipation	$P_{G(AV)}$	0.1	W
Peak gate power	$P_{GM}$	0.5	W

Thermal Resistance(between Junction and Case) @SOT-223-3L	$R_{\theta(J-C)}$	20 (Typ.)	°C/W
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Electronics Characteristics (T<sub>c</sub>=25°C Unless otherwise specified)

Parameter	Symbol	Min	Typ.	Max.	Unit
Gate Trigger Current (Continuous dc)@V <sub>D</sub> =12V, R <sub>L</sub> =33Ω	I <sub>GT</sub>	-	40	200	uA
Gate Trigger Voltage (Continuous dc) @V <sub>D</sub> =12V, R <sub>L</sub> =33Ω	V <sub>GT</sub>	-	0.5	1.5	V
Gate non-trigger voltage@V <sub>D</sub> =V <sub>DRM</sub> , T <sub>j</sub> =110°C	V <sub>GD</sub>	0.2	-	-	V
Holding Current@I <sub>T</sub> =50mA	I <sub>H</sub>	-	-	3	mA
Latching Current@I <sub>G</sub> =1.2I <sub>GT</sub>	I <sub>L</sub>	-	-	4	mA
Critical Rate-of-Rise of Off State Voltage@V <sub>D</sub> =0.66×V <sub>DRM</sub> , T <sub>j</sub> =110°C, R <sub>GK</sub> =1 KΩ	dV/dt	10	-	-	V/μs
Peak Forward On-State Voltage@I <sub>TM</sub> =4A, t <sub>p</sub> =380μs, T <sub>j</sub> =25°C	V <sub>TM</sub>	-	-	1.55	V
Peak Repetitive Forward@V <sub>DRM</sub> =V <sub>RRM</sub> , R <sub>GK</sub> =1 KΩ T <sub>j</sub> =25°C	I <sub>DRM</sub>	-	-	5	μA
Reverse Blocking Current@V <sub>DRM</sub> =V <sub>RRM</sub> , R <sub>GK</sub> =1 KΩ T <sub>j</sub> =110°C	I <sub>RRM</sub>	-	-	100	μA

Note: The above typical parameters or typical characteristics are only indicative and do not make specific guarantees. If detailed values are required, additional communication and provision are required.

FIG.1: Maximum power dissipation versus RMS on-state current

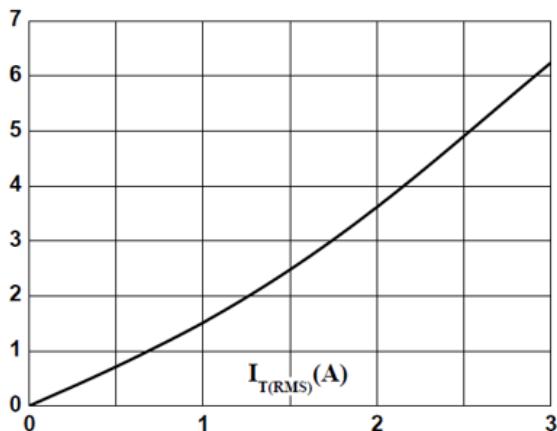
 $P(w)$ 

FIG.3: Surge peak on-state current versus number of cycles

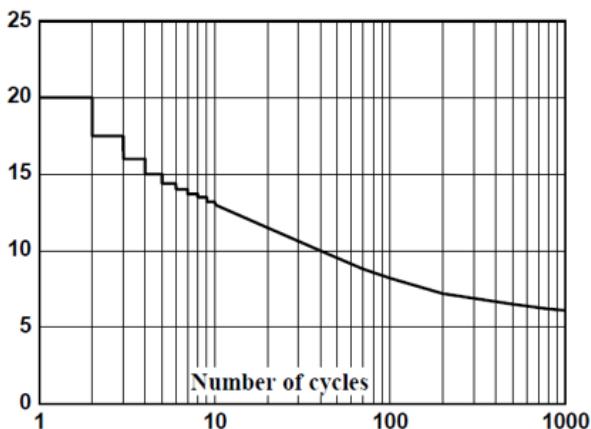
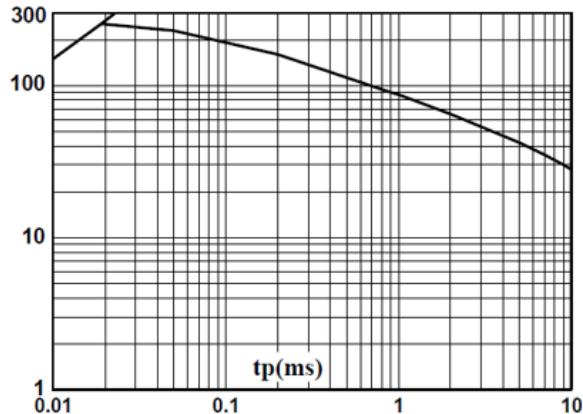
 $I_{TSM}(A)$ FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $tp < 10ms$  $I_{TSM}(A)$ 

FIG.2: RMS on-state current versus case temperature

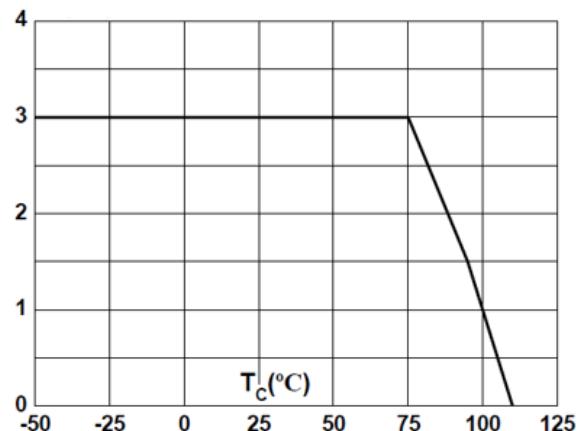
 $I_{T(RMS)}(A)$ 

FIG.4: On-state characteristics (maximum values)

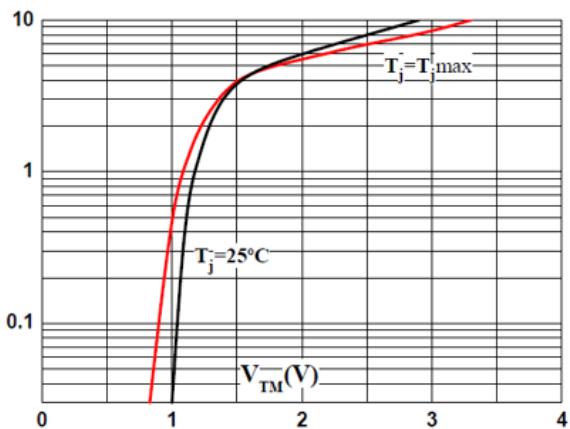
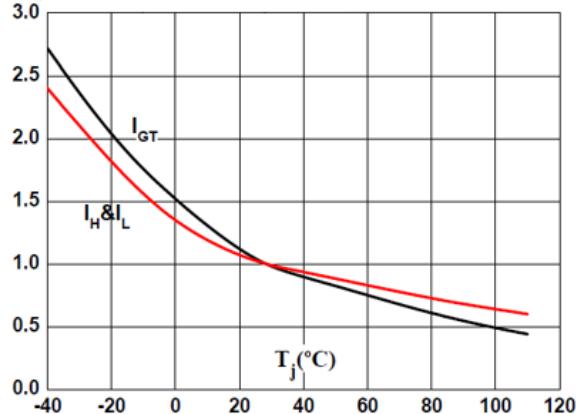
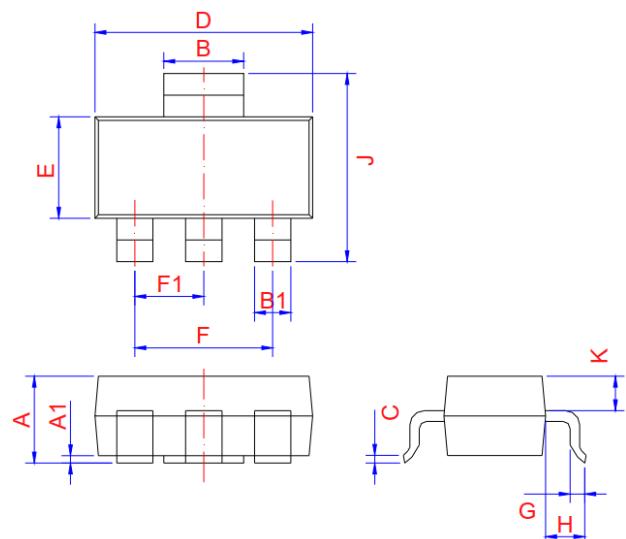
 $I_{TM}(A)$ 

FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

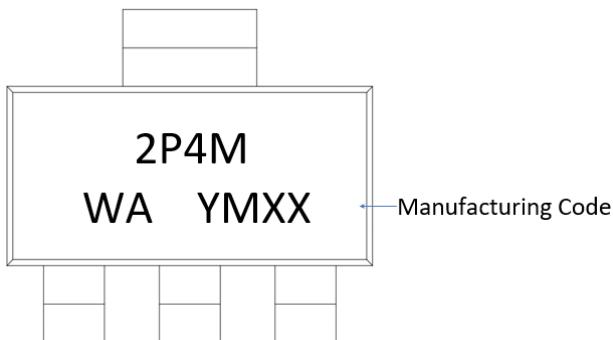
 $I_{GT}, I_H, I_L(T_j) / I_{GT}, I_H, I_L(T_j = 25^\circ C)$ 

## Outline Drawing

SYMBOL	MM		
	MIN	NOM	MAX
A	1.40	-	1.95
A1	0.01	-	0.15
B	2.80	3.00	3.20
B1	0.60	-	0.84
C	0.20	0.26	0.37
D	6.20	6.50	6.95
E	3.30	3.50	3.70
F	-	4.60	-
F1	-	2.30	-
G	0.60	-	1.20
H	1.50	1.75	2.00
J	6.65	7.00	7.30
K	-	0.90	-



## Marking Code



## Package Information

Package	Base qty.	Delivery mode
SOT-223-3L	2500	Reel

## Contact Information

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The product specification aims to provide users with a reference regarding various product parameters, performance, and usage. It presents certain aspects of the product's performance in graphical form and is intended solely for users to select product and make product comparisons, enabling users to better understand and evaluate the characteristics and advantages of the product. It does not constitute any commitment, warranty, or guarantee.

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