WLN2803CG

HIGH VOLTAGE, HIGH CURRENT DARLINGTON TRANSISTOR ARRAYS

Description

The WLN2803CG are high voltage, high current Darlington arrays each containing eight open collector common emitter pairs. Each pair is rated at 500mA. Suppression diodes are included for inductive load driving, the inputs and outputs are pinned in opposition to simplify board layout.

Features

- ESD Capability：8KV(HBM)
- 500mA Rated Collector Current (Single Output)
- High Voltage Outputs: 50V
- Output Clamp Diodes
- Inputs Compatible with Popular Logic Types (5V TTL,CMOS)
- Relay Driver Applications

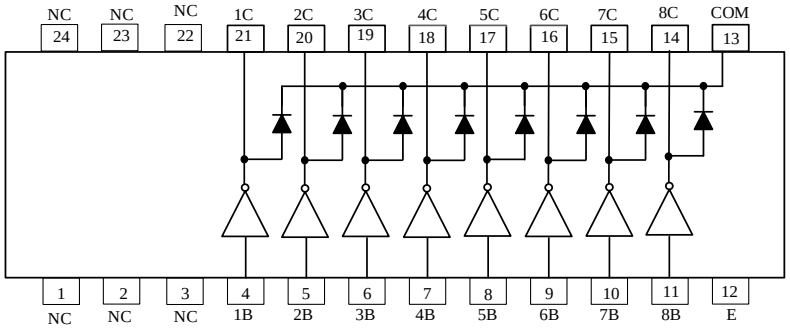
Mechanical Characteristics

- JEDEC QSOP24 package
- Marking: Marking Code
- Packaging: Tape and Reel
- RoHS Compliant & HF
- Device meets MSL3 requirement

Applications

- Solenoids
- Relays
- DC motors
- LED displays
- Filament lamps
- Thermal print-heads
- High-power buffers

Functional Diagram

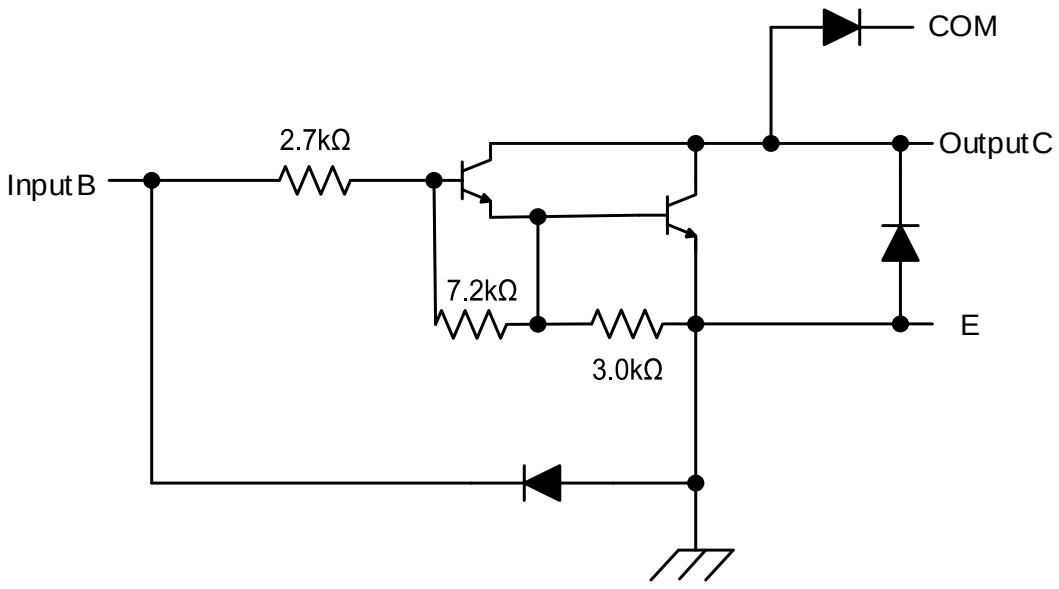


QSOP24 (Top View)

Pin Descriptions

Pin Number	Pin Name	Function
1	NC	NC
2	NC	NC
3	NC	NC
4	1B	Input Pair 1
5	2B	Input Pair 2
6	3B	Input Pair 3
7	4B	Input Pair 4
8	5B	Input Pair 5
9	6B	Input Pair 6
10	7B	Input Pair 7
11	8B	Input Pair 8
12	E	Common Emitter (Ground)
13	COM	Common Clamp Diodes
14	8C	Output Pair 8
15	7C	Output Pair 7
16	6C	Output Pair 6
17	5C	Output Pair 5
18	4C	Output Pair 4
19	3C	Output Pair 3
20	2C	Output Pair 2
21	1C	Output Pair 1
22	NC	NC
23	NC	NC
24	NC	NC

Functional Block Diagram



Absolute Maximum Ratings (Note 1) (@TA = +25°C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit
V_{CC}	Collector to Emitter Voltage	50	V
V_R	Clamp Diode Reverse Voltage (Note 2)	50	V
V_I	Input Voltage (Note 2)	30	V
I_{CP}	Peak Collector Current	500	mA
I_{OK}	Output Clamp Current	500	mA
I_{TE}	Total Emitter Current	-2.5	A
T_J	Junction Temperature	+150	°C
T_{STG}	Storage Temperature	-65 to +150	°C

Notes:
1. Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only. Functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
2. All voltage values are with respect to the emitter/substrate terminal E, unless otherwise noted.

Recommended Operating Conditions

Symbol	Parameter	MIN	MAX	Unit
V_{CC}	Collector to Emitter Voltage	-	50	V
T_A	Operating Ambient Temperature	-40	+105	°C

Switching Characteristics (@TA = +25°C, unless otherwise specified.)

Parameter		Test Figure	Min	Typ	Max	Unit
t_{PLH}	Propagation Delay Time, Low to High Level Output	9	-	0.25	1	µs
t_{PHL}	Propagation Delay Time, High to Low Level Output	9	-	0.25	1	µs
V_{OH}	High Level Output Voltage after Switching	9 (V_S = 50V, I_O = 300mA)	V_S-20	-	-	mV

Switching Characteristics (@TA = -40 to +105°C, unless otherwise specified.)

Parameter		Test Figure	Min	Typ	Max	Unit
t_{PLH}	Propagation Delay Time, Low to High Level Output	9	-	1	10	µs
t_{PHL}	Propagation Delay Time, High to Low Level Output	9	-	1	10	µs
V_{OH}	High Level Output Voltage after Switching	9 (V_S = 50V, I_O = 300mA)	V_S-50	-	-	mV

Electrical Characteristics (Cont.) (@TA = +25°C, unless otherwise specified.)

Parameter		Test Figure	Test Conditions		Min	Typ	Max	Unit
$V_{I(ON)}$	On State Input Voltage	6	V_{CE} = 2V	I_C = 200mA	-	-	2.4	V
				I_C = 250mA	-	-	2.7	
				I_C = 300mA	-	-	3	
$V_{CE(SAT)}$	Collector Emitter Saturation Voltage	5	I_I = 250μA, I_C = 100mA		-	0.9	1.1	V
			I_I = 350μA, I_C = 200mA		-	1	1.3	
			I_I = 500μA, I_C = 350mA		-	1.2	1.6	
V_F	Clamp Forward Voltage	8	I_F = 350mA		-	1.7	2	V
I_{CEX}	Collector Cut-off Current	1	V_{CE} = 50V, I_I = 0		-	-	50	μA
		2	V_{CE} = 50V, T_A = +105°C	I_I = 0	-	-	100	
$I_{I(OFF)}$	Off State Input Current	3	V_{CE} = 50V, I_C = 500μA		50	65	-	μA
I_I	Input Current	4	V_I = 3.85V		-	0.93	1.35	mA
I_R	Clamp Reverse Current	7	V_R = 50V	T_A = +105°C	-	-	100	μA
		-		-	-	-	50	
C_I	Input Capacitance	-	V_I = 0, f = 1MHz		-	15	25	pF

Electrical Characteristics (Cont.) (@TA = -40°C to +105°C, unless otherwise specified.)

Parameter		Test Figure	Test Conditions		Min	Typ	Max	Unit
$V_{I(ON)}$	On State Input Voltage	6	V_{CE} = 2V	I_C = 200mA	-	-	2.7	V
				I_C = 250mA	-	-	2.9	
				I_C = 300mA	-	-	3	
$V_{CE(SAT)}$	Collector Emitter Saturation Voltage	5	I_I = 250μA, I_C = 100mA		-	0.9	1.2	V
			I_I = 350μA, I_C = 200mA		-	1	1.4	
			I_I = 500μA, I_C = 350mA		-	1.2	1.7	
V_F	Clamp Forward Voltage	8	I_F = 350mA		-	1.7	2.2	V
I_{CEX}	Collector Cut-off Current	1	V_{CE} = 50V, I_I = 0		-	-	100	μA
$I_{I(OFF)}$	Off State Input Current	3	V_{CE} = 50V, I_C = 500μA		30	65	-	μA
I_I	Input Current	4	V_I = 3.85V		-	0.93	1.35	mA
I_R	Clamp Reverse Current	7	V_R = 50V		-	-	100	μA
C_I	Input Capacitance	-	V_I = 0, f = 1MHz		-	15	25	pF

Parameter Measurement Circuits

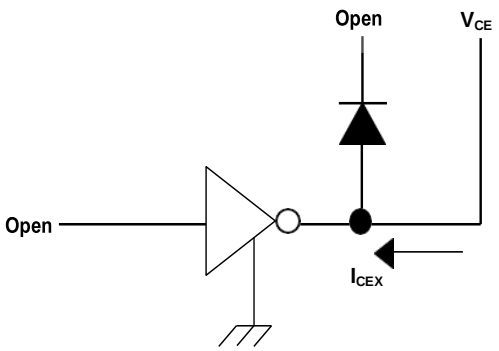


Fig.1 I_{CEX} Test Circuit

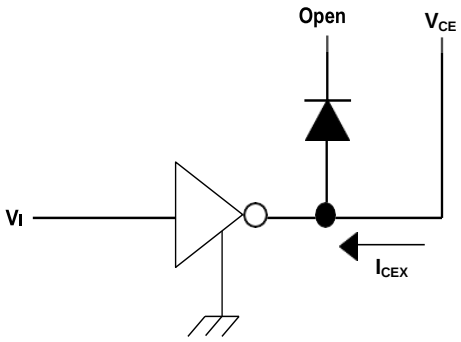


Fig.2 I_{CEX} Test Circuit

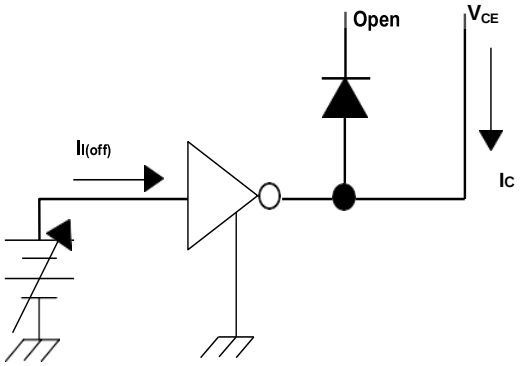


Fig.3 $I_{I(off)}$ Test Circuit

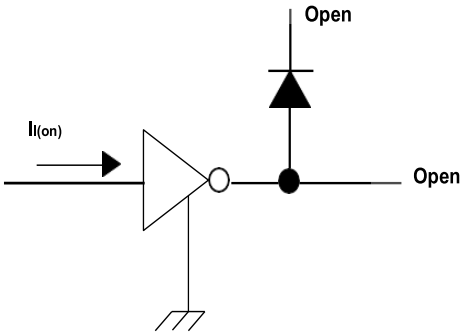


Fig.4 I_I Test Circuit

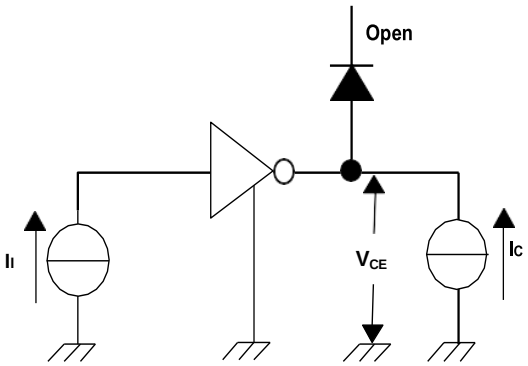


Fig.5 h_{FE}, $V_{CE(sat)}$ Test Circuit

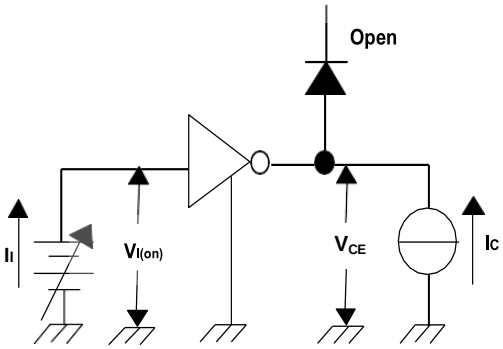


Fig.6 $V_{I(on)}$ Test Circuit

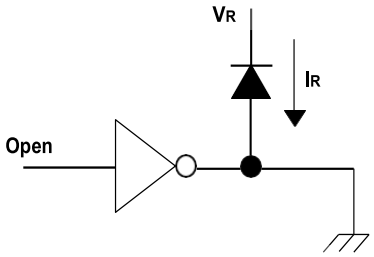


Fig.7 I_R Test Circuit

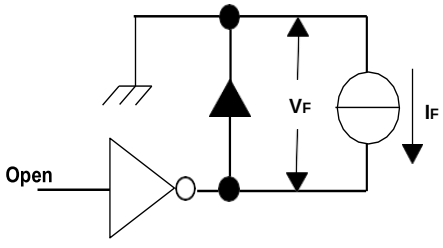


Fig.8 V_F Test Circuit

Parameter Measurement Circuits (Cont.)

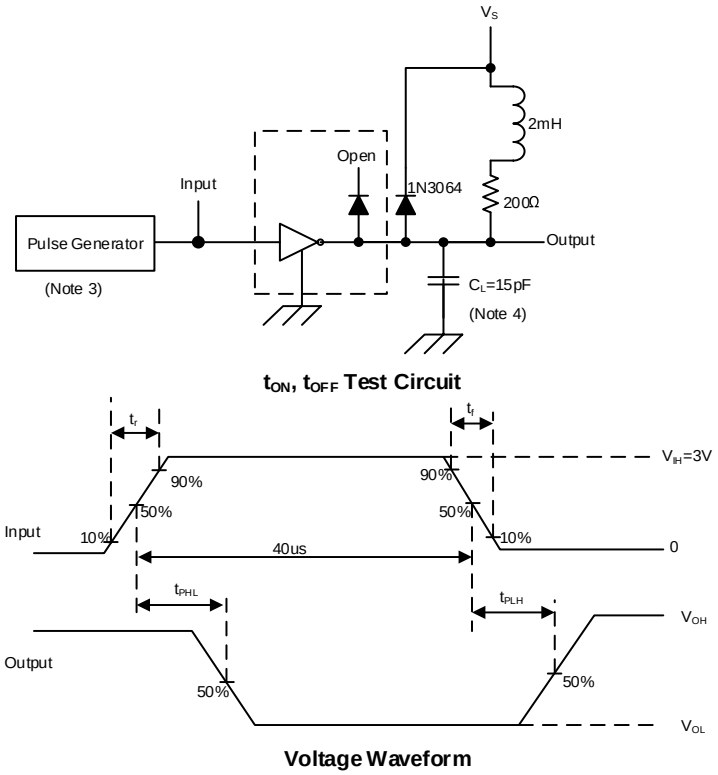


Fig. 9 Latch-Up Test Circuit and Voltage Waveform

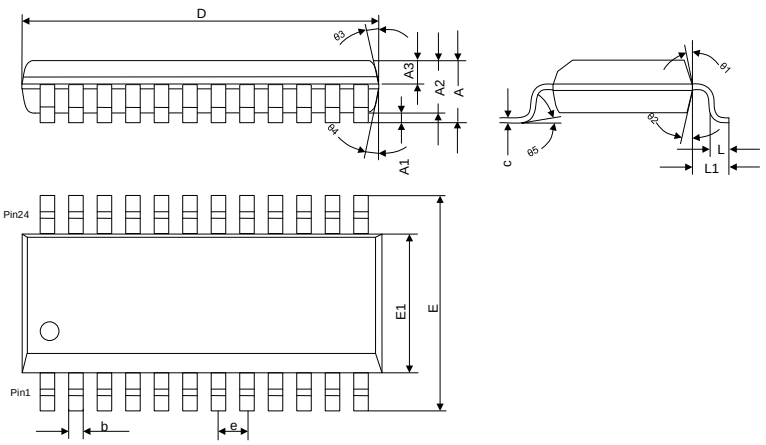
Notes:

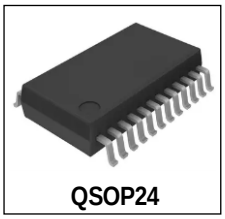
3 The pulse generator has the following characteristics: Pulse Width = 12.5Hz, output impedance 50Ω, tr ≤ 5ns, tr ≤ 10ns.

4 C_L includes prove and jig capacitance.

Outline Drawing – QSOP24

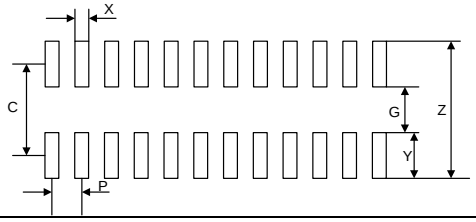
PACKAGE OUTLINE





Symbol	MILLIMETERS		
	MIN	NOM	MAX
A	-	-	1.70
A1	0.10	0.15	0.21
A2	1.40	1.45	1.50
A3	0.60	0.65	0.70
b	0.21	-	0.29
c	0.18	-	0.23
D	8.45	8.60	8.75
E	5.80	6.00	6.20
E1	3.80	3.85	3.90
e	0.635BSC		
L	0.58	0.60	0.63
L1	1.05 BSC		
θ1	8°	-	15°
θ2	8°	-	15°
θ3	8°	-	15°
θ4	8°	-	15°
θ5	0°	-	6°

Soldering Footprint



DIM	MILLIMETERS
C	5.60
G	3.80
P	0.635
X	0.29
Y	1.8
Z	7.40

Marking Codes

Part Number	WLN2803CG
Marking Code	

WLN2803CG=Specific Device Code
XXXX=Lot Code

Package Information

Qty：4k/Reel

CONTACT INFORMATION

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Product Specification Statement

1. The product specification aims to provide users with a reference regarding various product parameters, performance, and usage. It presents certain aspects of the product's performance in graphical form and is intended solely for users to select product and make product comparisons, enabling users to better understand and evaluate the characteristics and advantages of the product. It does not constitute any commitment, warranty, or guarantee.

2. The product parameters described in the product specification are numerical values, characteristics, and functions obtained through actual testing or theoretical calculations of the product in an independent or ideal state. Due to the complexity of product applications and variations in test conditions and equipment, there may be slight fluctuations in parameter test values. WAYON shall not guarantee that the actual performance of the product when installed in the customer's system or equipment will be entirely consistent with the product specification, especially concerning dynamic parameters. It is recommended that users consult with professionals for product selection and system design. Users should also thoroughly validate and assess whether the actual parameters and performance when installed in their respective systems or equipment meet their requirements or expectations. Additionally, users should exercise caution in verifying product compatibility issues, and WAYON assumes no responsibility for the application of the product.

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4. Users are advised to pay attention to the parameter limit values specified in the product specification and maintain a certain margin in design or application to ensure that the product does not exceed the parameter limit values defined in the product specification. This precaution should be taken to avoid exceeding one or more of the limit values, which may result in permanent irreversible damage to the product, ultimately affecting the quality and reliability of the system or equipment.

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