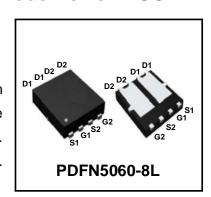


80V Dual N-Channel Enhancement Mode Power MOSFET

Description

WMB105DN08LG4 uses Wayon's 4th generation power trench MOSFET technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance. This device is well suited for high efficiency fast switching applications.

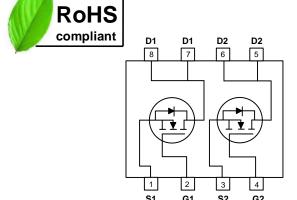


Features

- $V_{DS} = 80V$, $I_D = 48A$ Typ. $R_{DS(on)} = 10.0 m\Omega$ @ $V_{GS} = 10V$ Typ. $R_{DS(on)} = 12.5 m\Omega$ @ $V_{GS} = 4.5V$
- Green Device Available
- 100% EAS Guaranteed
- RoHS Compliant & Halogen-Free

Applications

- Load Switch
- Power Management Switches



Absolute Maximum Ratings (T_A = 25°C, unless otherwise noted)

| Parameter | | Symbol | Value | Unit | |
|--|-----------------------|-----------------------------------|------------|------|--|
| Drain-Source Voltage | | V _{DS} | 80 | V | |
| Gate-Source Voltage | | V _{GS} | ±20 | V | |
| | T _C =25°C | | 48 | _ | |
| Continuous Drain Current | T _C =100°C | lσ | 30 | A | |
| Pulsed Drain Current ¹ | | Ірм | 192 | А | |
| Single Pulse Avalanche Energy ² | | EAS | 64.8 | mJ | |
| Total Power Dissipation | T _C =25°C | P _D | 52 | W | |
| Operating Junction and Storage Temperature Range | | T _J , T _{STG} | -55 to 150 | °C | |

Thermal Characteristics

| Parameter | Symbol | Value | Unit |
|--|--------|-------|------|
| Thermal Resistance from Junction-to-Ambient ³ | Reja | 52 | °C/W |
| Thermal Resistance from Junction-to-Case | Rелс | 2.4 | °C/W |

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Electrical Characteristics (T_J = 25°C, unless otherwise noted)

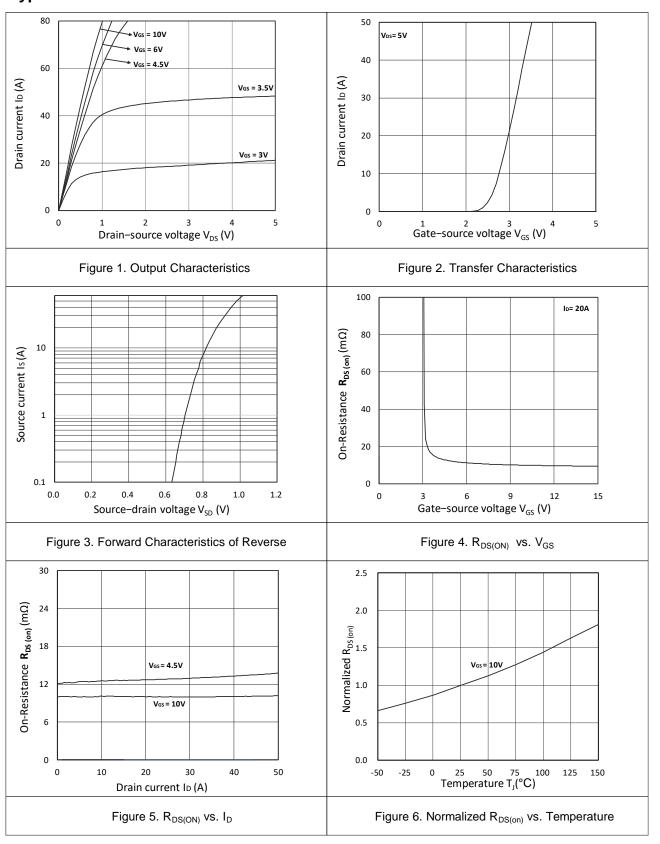
| Parameter | | Symbol | Test Conditions | Min. | Тур. | Max. | Unit |
|--|---------------------------------------|----------------------|---|------|------|------|-------|
| Static Characteristics | | | | 1 | | | |
| Drain-Source Breakdown Voltage | | V _{(BR)DSS} | V _{GS} = 0V, I _D = 250µA | 80 | - | - | V |
| Gate-Body Leakage Current | | I _{GSS} | $V_{DS} = 0V, V_{GS} = \pm 20V$ | - | - | ±100 | nA |
| Zero Gate Voltage Drain Current | T _J =25°C | I _{DSS} | V _{DS} = 80V, V _{GS} = 0V | - | - | 1 | μА |
| | T _J =100°C | | | - | - | 100 | |
| Gate-Threshold Voltage | | V _{GS(th)} | $V_{DS} = V_{GS}$, $I_D = 250\mu A$ | 1.4 | 1.8 | 2.2 | V |
| Drain-Source on-Resistance ⁴ | | _ | V _{GS} = 10V, I _D = 20A | - | 10 | 12.5 | mΩ |
| | | R _{DS(on)} | V _{GS} = 4.5V, I _D = 10A | - | 12.5 | 15.5 | |
| Forward Transconductance | Forward Transconductance ⁴ | | V _{DS} = 10V, I _D = 20A | - | 44 | - | S |
| Dynamic Characteristic | S ⁵ | | | | | | |
| Input Capacitance | | Ciss | | - | 1235 | - | |
| Output Capacitance Reverse Transfer Capacitance | | Coss | V _{DS} = 40V, V _{GS} =0V, f =1MHz | - | 375 | - | pF |
| | | Crss | | - | 25 | - | |
| Gate Resistance | | R _G | f =1MHz | - | 1.9 | - | Ω |
| Switching Characteristic | CS ⁵ | | | | | | |
| Total Gate Charge | | Qg | | - | 23 | - | |
| Gate-Source Charge Gate-Drain Charge | | Q_{gs} | $V_{GS} = 10V, V_{DS} = 40V,$ $I_{D} = 20A$ | - | 3.6 | - | nC |
| | | Q_{gd} | | - | 4.6 | - | |
| Turn-on Delay Time | | t _{d(on)} | | - | 6.3 | - | |
| Rise Time | | t _r | $V_{GS} = 10V, V_{DD} = 40V,$ $R_{G} = 3\Omega, I_{D} = 20A$ | - | 5 | - | ns ns |
| Turn-off Delay Time | | t _{d(off)} | | - | 22 | - | |
| Fall Time | | t _f | | - | 5 | - | |
| Body Diode Reverse Recovery Time Body Diode Reverse Recovery Charge | | t rr | | - | 36 | - | ns |
| | | Qrr | - I _F = 20A, di/dt = 100A/μs | - | 32.4 | - | nC |
| Drain-Source Body Dioc | de Characte | eristics | | • | | | |
| Diode Forward Voltage ⁴ | | V _{SD} | I _S = 20A, V _{GS} = 0V | - | - | 1.2 | V |
| Continuous Source Current Tc=25°C | | ls | - | - | - | 48 | Α |

Notes:

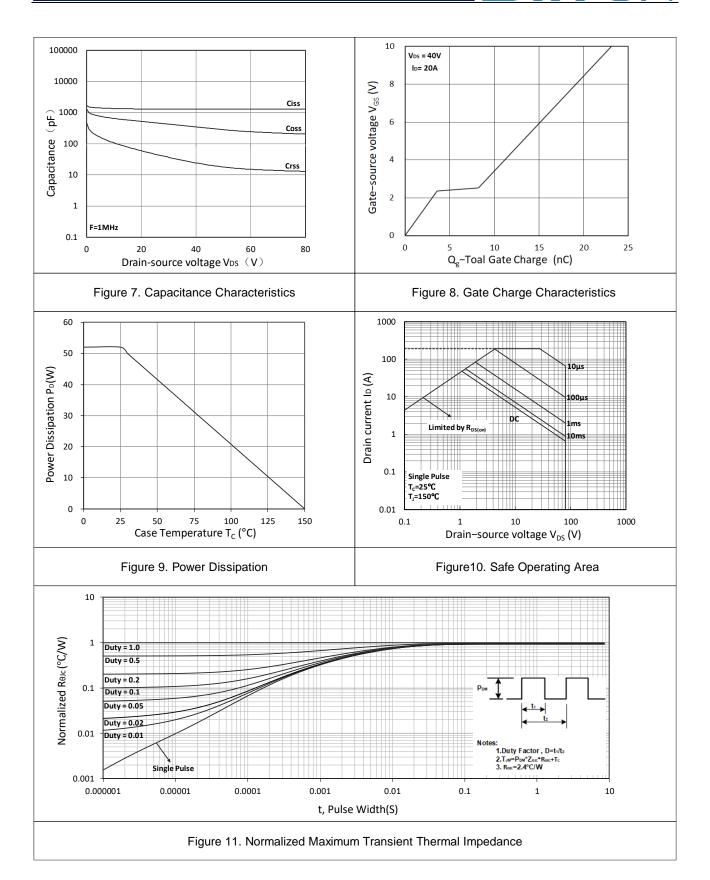
- 1. Repetitive rating, pulse width limited by junction temperature $T_{\text{J(MAX)}}$ =150°C.
- 2. The test condition is V_{DD} =25V, V_{GS} =10V, L=0.4mH, I_{AS} =18A.
- 3. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
- 4. The data tested by pulsed, pulse width \leq 300us, duty cycle \leq 2%.
- 5. This value is guaranteed by design hence it is not included in the production test.



Typical Characteristics









Test Circuit

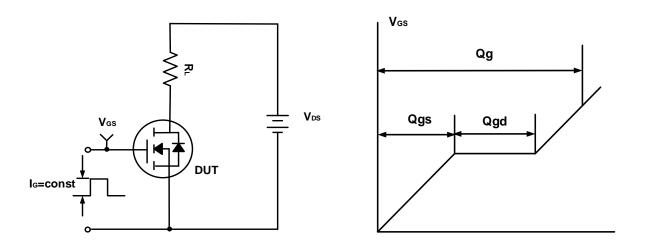


Figure A. Gate Charge Test Circuit & Waveforms

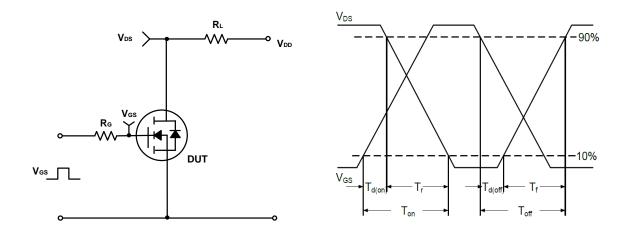


Figure B. Switching Test Circuit & Waveforms

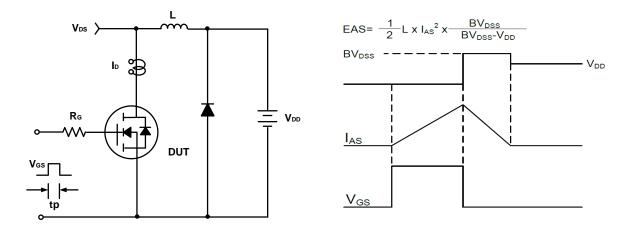
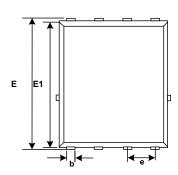
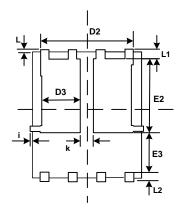


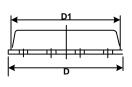
Figure C. Unclamped Inductive Switching Circuit & Waveforms

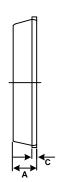


Mechanical Dimensions for PDFN5060-8L









COMMON DIMENSIONS

| SYMBOL | MM | | | |
|--------|---------|------|--|--|
| | MIN | MAX | | |
| А | 0.90 | 1.17 | | |
| b | 0.33 | 0.51 | | |
| С | 0.20BSC | | | |
| D | 4.80 | 5.40 | | |
| D1 | 4.80 | 5.00 | | |
| D2 | 3.95 | 4.45 | | |
| D3 | 1.55 | 1.80 | | |
| E | 5.90 | 6.15 | | |
| E1 | 5.65 | 5.85 | | |
| E2 | 3.30 | 3.78 | | |
| E3 | 1.10 | / | | |
| е | 1.27BSC | | | |
| L | 0.05 | 0.25 | | |
| L1 | 0.38 | 0.61 | | |
| L2 | 0.38 | 0.71 | | |
| i | / | 0.18 | | |
| k | 0.50 | 0.70 | | |

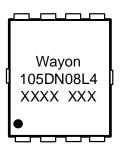
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Ordering Information

| Part Package | | Marking | Packing method | |
|---------------|-------------|-----------|----------------|--|
| WMB105DN08LG4 | PDFN5060-8L | 105DN08L4 | Tape and Reel | |

Marking Information



105DN08L4 = Device code

XXXX XXX= Date code

Contact Information

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WAYON website: http://www.way-on.com

For additional information, please contact your local Sales Representative.

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Product Specification Statement

- 1. The product specification aims to provide users with a reference regarding various product parameters, performance, and usage. It presents certain aspects of the product's performance in graphical form and is intended solely for users to select product and make product comparisons, enabling users to better understand and evaluate the characteristics and advantages of the product. It does not constitute any commitment, warranty, or guarantee.
- 2. The product parameters described in the product specification are numerical values, characteristics, and functions obtained through actual testing or theoretical calculations of the product in an independent or ideal state. Due to the complexity of product applications and variations in test conditions and equipment, there may be slight fluctuations in parameter test values. WAYON shall not guarantee that the actual performance of the product when installed in the customer's system or equipment will be entirely consistent with the product specification, especially concerning dynamic parameters. It is recommended that users consult with professionals for product selection and system design. Users should also thoroughly validate and assess whether the actual parameters and performance when installed in their respective systems or equipment meet their requirements or expectations. Additionally, users should exercise caution in verifying product compatibility issues, and WAYON assumes no responsibility for the application of the product.
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- 4. Users are advised to pay attention to the parameter limit values specified in the product specification and maintain a certain margin in design or application to ensure that the product does not exceed the parameter limit values defined in the product specification. This precaution should be taken to avoid exceeding one or more of the limit values, which may result in permanent irreversible damage to the product, ultimately affecting the quality and reliability of the system or equipment.
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