

30V N-Channel Enhancement Mode Power MOSFET

Description

WMQ90N03TA uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

PDFN3030-8L

Features

- $V_{DS} = 30V, I_D = 96A$
 - Typ. $R_{DS(on)} = 2.3 \text{m}\Omega$ @ $V_{GS} = 10V$
 - Typ. $R_{DS(on)} = 3.4 \text{m}\Omega$ @ $V_{GS} = 4.5 \text{V}$
- Green Device Available
- 100% EAS Guaranteed
- RoHS Compliant & Halogen-Free

Applications

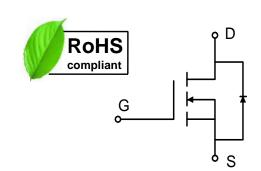
- Motor Driver
- Power Management Switches

Absolute Maximum Ratings (T_A= 25°C, unless otherwise noted)

Parameter		Symbol	Value	Unit	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		V _{GS}	±20	V	
Continuous Drain Current	T _C =25°C	lο	96	А	
	T _C =100°C		60		
Pulsed Drain Current ¹		Ірм	384	А	
Single Pulse Avalanche Energy ²		EAS	135.2	mJ	
Total Power Dissipation	Tc=25°C	P _D	44.6	W	
Operating Junction and Storage Temperature Range		TJ, TSTG	-55 to 150	°C	

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ³	Reja	58	°C/W
Thermal Resistance from Junction-to-Case	ReJc	2.8	°C/W





Electrical Characteristics (T_J = 25°C, unless otherwise noted)

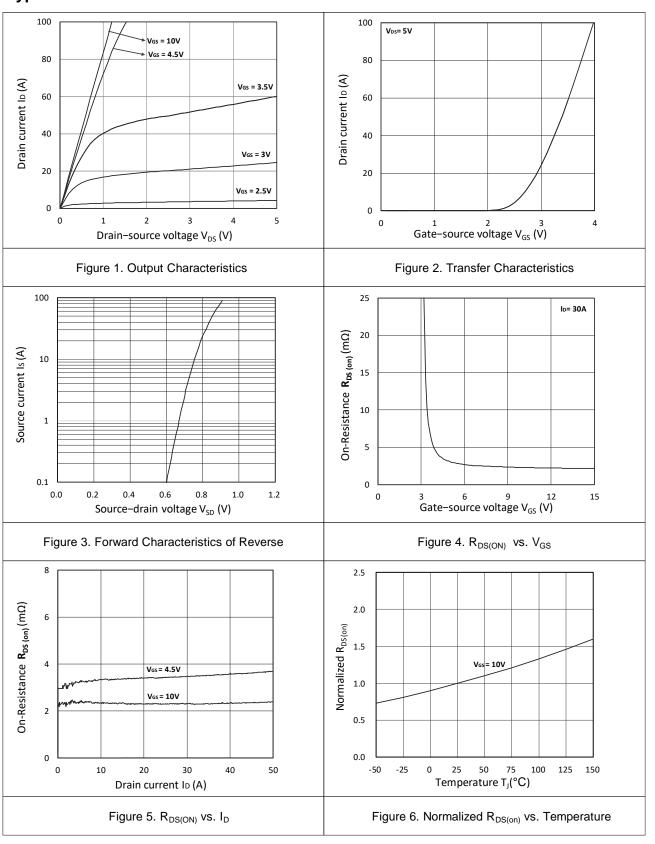
Parameter		Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static Characteristics				•				
Drain-Source Breakdown Voltage		V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	30	-	-	V	
Gate-body Leakage Current		Igss	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA	
Zero Gate Voltage Drain Current	TJ=25°C	- I _{DSS}	V _{DS} = 30V, V _{GS} = 0V	-	-	1	μА	
	T _J =100°C			-	-	100		
Gate-Threshold Voltage	Gate-Threshold Voltage		V _{DS} = V _{GS} , I _D = 250μA	1.2	1.65	2.2	V	
Drain Course On Besistance4		Б	V _{GS} = 10V, I _D = 30A	-	2.3	3		
Drain-Source On-Resistance⁴		R _{DS(on)}	V _{GS} = 4.5V, I _D = 20A	-	3.4	4.5	mΩ	
Forward Transconductance ⁴		g fs	V _{DS} = 10V, I _D = 30A	-	68	-	S	
Dynamic Characteristics5								
Input Capacitance		C _{iss}		-	3400	-		
Output Capacitance Reverse Transfer Capacitance		Coss	V _{DS} = 15V, V _{GS} =0V, f =1MHz	-	453	-	pF	
		Crss		-	368	-		
Gate Resistance		Rg	f =1MHz	-	2.1	-	Ω	
Switching Characteristics	5 5							
Total Gate Charge	Total Gate Charge Q _g		$V_{GS} = 10V, V_{DS} = 15V,$ $I_{D} = 30A$	-	67	-	nC	
Gate-Source Charge Gate-Drain Charge		Qgs		-	10.5	-		
		Q_{gd}		-	11.8	-		
Turn-On Delay Time		t _{d(on)}		-	9	-		
Rise Time		tr	$V_{GS} = 10V, V_{DD} = 15V,$ $R_{G} = 3\Omega, I_{D} = 30A$	-	8	-	. ns	
Turn-Off Delay Time		t _{d(off)}		-	55	-		
Fall Time		t _f		-	21.5	-		
Body Diode Reverse Recovery Time		t _{rr}	1 00A 17/16 100A	-	30	-	ns	
Body Diode Reverse Recovery Charge		Qrr	I _F = 30A, di/dt = 100A/μs	-	12	-	nC	
Drain-Source Body Diode	Characteri	stics	•	•	•	•		
Diode Forward Voltage ⁴		V _{SD}	Is = 20A, V _G S = 0V	-	-	1.2	V	
Continuous Source Current	T _C =25°C	Is	-	-	-	96	А	

Note:

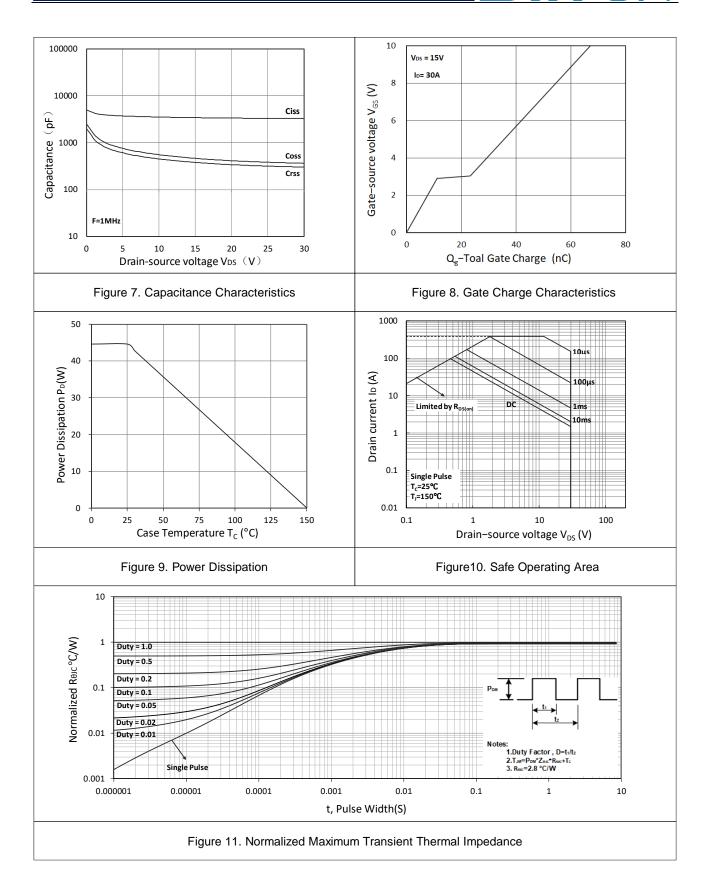
- 1. Repetitive rating, pulse width limited by junction temperature $T_{\text{J(MAX)}}$ =150°C
- 2. The test condition is $V_{\text{DD}}\text{=}$ 25V, $V_{\text{GS}}\text{=}$ 10V, L= 0.4mH, $I_{\text{AS}}\text{=}$ 26A.
- 3. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
- 4. The data tested by pulsed, pulse width \leq 300us, duty cycle \leq 2%.
- 5. This value is guaranteed by design hence it is not included in the production test.



Typical Characteristics









Test Circuit

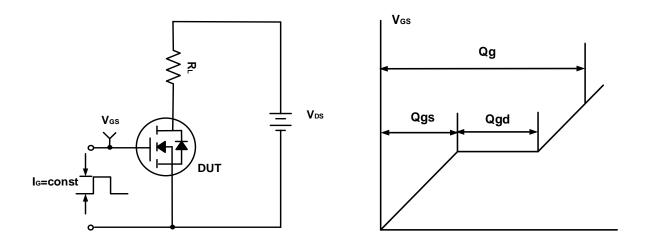


Figure A. Gate Charge Test Circuit & Waveforms

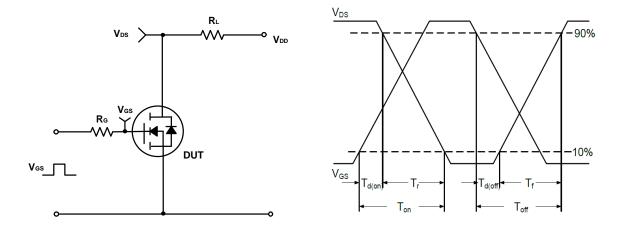


Figure B. Switching Test Circuit & Waveforms

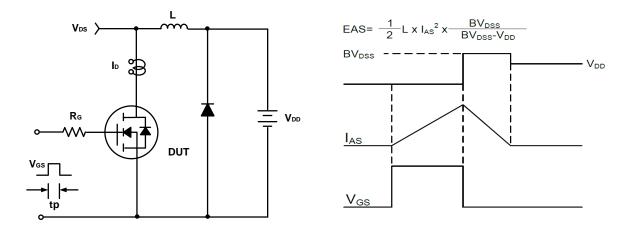
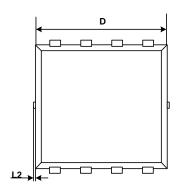
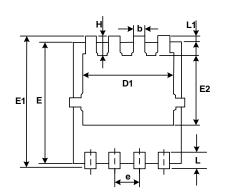


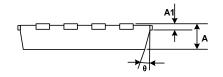
Figure C. Unclamped Inductive Switching Circuit & Waveforms



Mechanical Dimensions for PDFN3030-8L







COMMON DIMENSIONS

CVMDOL	MM			
SYMBOL	MIN	MAX		
А	0.65	0.90		
A1	0.10	0.25		
D	2.90	3.30		
D1	2.25	2.69		
E	2.90	3.20		
E1	3.00	3.60		
E2	1.35	2.20		
b	0.20	0.40		
е	0.65BSC			
L	0.15	0.50		
L1	0.13BSC			
L2	0.00	0.20		
Н	0.15	0.65		
θ	0°	14°		



Ordering Information

Part	Package	Marking	Packing method
WMQ90N03TA	PDFN3030-8L	Q90N03A	Tape and Reel

Marking Information



Q90N03A = Device code

XXXXXXX = Date code

Contact Information

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- 2. The product parameters described in the product specification are numerical values, characteristics, and functions obtained through actual testing or theoretical calculations of the product in an independent or ideal state. Due to the complexity of product applications and variations in test conditions and equipment, there may be slight fluctuations in parameter test values. WAYON shall not guarantee that the actual performance of the product when installed in the customer's system or equipment will be entirely consistent with the product specification, especially concerning dynamic parameters. It is recommended that users consult with professionals for product selection and system design. Users should also thoroughly validate and assess whether the actual parameters and performance when installed in their respective systems or equipment meet their requirements or expectations. Additionally, users should exercise caution in verifying product compatibility issues, and WAYON assumes no responsibility for the application of the product.
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